

# PHILIPS



Digital multimeter

**PM2423**

9447 024 23011

9499 470 06311

730101/1/01

# PHILIPS

Manual

Digital multimeter **PM2423**

9447 024 23011



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## I. Introduction

## GENERAL

The PM 2423 is a digital multimeter with a 3+1 digit display with a maximum reading of 1999.

The instrument can be employed for measuring:

- DC voltages of 1 mV up to 1000 V
- AC voltages of 1 mV up to 350 V<sub>rms</sub>
- Resistances of 0,1  $\Omega$  up to 200 M $\Omega$

The selection of the measuring ranges is effected automatically on the DC and AC voltage ranges and semi-automatically on the resistance range.

The polarity of the DC voltages and the position of the decimal point is indicated automatically.

Range and display hold is possible.

On account of the specifications, accuracy and construction the instrument is an ideal general-purpose multimeter for production lines or laboratories, for servicing as well as for education.

## II. Technical data

Properties expressed in numerical values with tolerances stated, are guaranteed by the manufacturer. Numerical values without tolerances serve only for information and represent the properties of an average instrument.

The data mentioned below are guaranteed for six months at a temperature of 23 °C  $\pm$ 10 °C and humidity below 85 %.

### A. MEASURING RANGES

#### 1. Direct voltages

Measuring range	4 ranges: 2 V, 20 V, 200 V and 1000 V end range
Resolution	1 mV on the 2 V range
Input impedance	1 M $\Omega$ on the 2 V range 10 M $\Omega$ on the 20 V up to the 1000 V range
Accuracy	$\pm$ 0.1 % of reading $\pm$ 1 digit
Response time	0,4 sec. without range switching
Maximum permissible voltage	DC 1000 V

#### 2. Alternating voltages

Measuring range	4 ranges: 2 V, 20 V, 200 V and 350 V <sub>rms</sub> end range
Resolution	1 mV <sub>rms</sub> on the 2 V range

Input impedance	1 M $\Omega$ on all ranges
Frequency range	45 Hz to 20 kHz
Accuracy	$\pm 0,3$ % of reading $\pm 0,2$ % of end range on the 2, 20 and 200 V range $\pm 0,5$ % of reading $\pm 0,5$ % of end range on the 350 V range
Response time	2 secs without range switching
Maximum permissible voltage	AC 350 V <sub>rms</sub>

### 3. Resistances

Measuring range	7 ranges: 200 $\Omega$ , 2 k $\Omega$ , 20 k $\Omega$ , 200 k $\Omega$ , 2 M $\Omega$ , 20 M $\Omega$ and 200 M $\Omega$ end range																
Resolution	0,1 $\Omega$ on the 200 $\Omega$ range																
Test voltage	2 V at end range																
Test current	<table> <thead> <tr> <th>Range</th> <th>Current</th> </tr> </thead> <tbody> <tr> <td>200 <math>\Omega</math></td> <td>10 mA</td> </tr> <tr> <td>2 k<math>\Omega</math></td> <td>1 mA</td> </tr> <tr> <td>20 k<math>\Omega</math></td> <td>100 <math>\mu</math>A</td> </tr> <tr> <td>200 k<math>\Omega</math></td> <td>10 <math>\mu</math>A</td> </tr> <tr> <td>2 M<math>\Omega</math></td> <td>1 <math>\mu</math>A</td> </tr> <tr> <td>20 M<math>\Omega</math></td> <td>100 nA</td> </tr> <tr> <td>200 M<math>\Omega</math></td> <td>10 nA</td> </tr> </tbody> </table>	Range	Current	200 $\Omega$	10 mA	2 k $\Omega$	1 mA	20 k $\Omega$	100 $\mu$ A	200 k $\Omega$	10 $\mu$ A	2 M $\Omega$	1 $\mu$ A	20 M $\Omega$	100 nA	200 M $\Omega$	10 nA
Range	Current																
200 $\Omega$	10 mA																
2 k $\Omega$	1 mA																
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200 k $\Omega$	10 $\mu$ A																
2 M $\Omega$	1 $\mu$ A																
20 M $\Omega$	100 nA																
200 M $\Omega$	10 nA																
Maximum permissible voltage between COM-OHM terminal	100 V DC or AC for 10 seconds																
Accuracy	$\pm 0,5$ % of reading $\pm 1$ digit on the 200 $\Omega$ up to the 200 k $\Omega$ range $\pm 1$ % of reading $\pm 1$ digit on the 2 M $\Omega$ range $\pm 2$ % of reading $\pm 1$ digit on the 20 M $\Omega$ range $\pm 10$ % of reading $\pm 1$ digit on the 200 M $\Omega$ range																

### B. GENERAL DATA

Operating system	Integrating system
Input terminals	Floating
Maximum display	1999 except on the DC 1000 V and AC 350 V range
Range switching	Automatic for DC and AC Automatic with manual $\Omega$ , k $\Omega$ and M $\Omega$ switching for resistance measurements
Measuring speed	2,5 measurements/sec.
Range switching time	400 msec./range
Common mode rejection	>60 dB at mains frequency
Series mode rejection	40 dB at mains frequency
Overrange indication	By indication $\text{\textcircled{R}}$ and continuous running of the display

Maximum permissible voltages	Between case - COM: 600 V d.c. continuous Between V - COM: 1000 V d.c. continuous 350 V <sub>rms</sub> continuous Between COM - OHM: 100 V d.c. or a.c. for 10 seconds
Polarity indication	Automatic
Hold	Polarity, display and range holding is possible
Operating temperature range	0 °C to 50 °C
Mains voltage	220 V <sub>rms</sub> +10 % freq. 50/60 Hz
Power consumption	approx. 11 VA

### C. MECHANICAL DATA

Dimensions	Width	220 mm
	Height	75 mm
	Depth	214 mm
Weight	approximately 2 kg	

## III. Accessories

- Set of measuring leads
- Fuse 0.1 A, delayed action
- Vinyl cover
- Manual

#### Optionally available

- HT probe PM 9240

This probe is suitable for measuring direct voltages up to 30 kV.

Maximum measuring error: approximately 15 % (in conjunction with PM 2423) in the 2 V range, approximately 6 % (in conjunction with PM 2423) in all other ranges.

Input resistance: 991 MΩ in the 2 V range

1000 MΩ in all other ranges

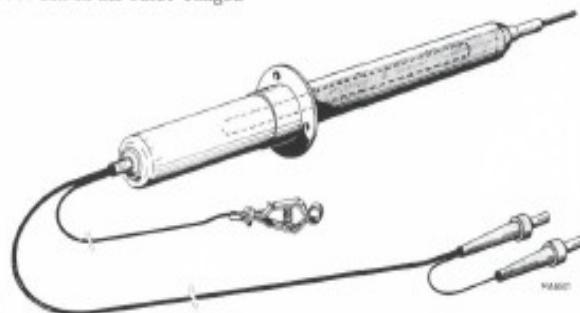


Fig. 1. HT probe PM 9240

## IV. Block diagram

### A. ANALOG SECTION

The analog section converts all input signals into a time, proportional to the relevant input signal which have been first normalized in a d.c. level of 2 V at end-of-range values by the input circuit. This is effected by the analog-to-digital converter.

#### 1. Input circuit

The input circuit normalizes all input signals into a d.c. level of 2 V at end of range. For the various measuring modes this is achieved as follows:

##### - DC

The d.c. voltage supplied to the instrument is amplified by amplifier A1, the gain of which can be varied by reed-relays.

##### - AC

The a.c. voltage supplied to the instrument is amplified by the same amplifier A1 after which the output signal is rectified by an a.c. to d.c. converter.

##### - Resistances

At resistance measurements a reference voltage (8.2 V) is supplied to amplifier A1, which has a gain of 1, 0.1 or 0.01, thus dividing the reference voltage. This voltage is supplied to amplifier A3 via a series resistor  $R_g$ , the value of which can be varied by relays.

As the unknown resistor is included in the feedback circuit of amplifier A3, the gain and thus the output voltage of A3 is determined by  $\frac{R_X}{R_S}$ .

#### 2. Analog-to-digital converter (Fig. 3)

The analog-to-digital converter of the PM 2423 is based on the dual slope principle (voltage-to-time conversion) and has an automatic zero correction.

The sampling time is 400 msec. and may be divided in:

##### - up-integration; the d.c. voltage supplied by the input circuit is integrated by an integrator circuit.

The up-integration time is a fixed time of 100 msec.

##### - down-integration; by supplying a reference voltage to the integrator its output voltage goes to zero level. As soon as the zero level is reached, a zero detecting (ZD) signal is supplied.

The down-integration time determines which value is indicated.

The maximum time is 200 msec. and is achieved at end-of-range values.

##### - zero correction; a second measurement is made, the input level during the second up-integration being 0 V.

The timing of the system is effected by the digital section.

### B. DIGITAL SECTION

#### 1. Control circuit

This circuit controls the ADC and thus determines which voltage is supplied to the integrator. The control signals are derived from the counter, the stop signal and the zero detecting signal.

#### 2. Stop

From the zero detecting signal supplied by the ADC, a stop signal is formed to set the control circuit to a certain position.



### 3. Memory

The memory signal, supplied at the completion of the down-integration, is used.

- as clock signal for the auto range circuit
- to block the main gate
- to produce the transfer signal
- as clock signal for the overrange display

### 4. Transfer

At the end of the down-integration the transfer pulse is initiated, so that the counter-information is stored in the memory.

### 5. Overload

If, in a certain hold range, the indication is higher than 1999 the overload condition is signalled by indication  $\textcircled{R}$  and in that time the information is not memorized.

### 6. Polarity

Upon completion of the up-integration the polarity is determined and indicated as soon as a transfer-pulse is supplied.

### 7. Counter and Memory

The counter counts the clock pulses (10 kHz) with three 10 decade counters. The information is supplied to the indicators via a memory as soon as a transfer pulse is supplied.

### 8. Auto range

If the indication in a selected range is lower than 180, a more sensitive range is selected; if the indication is higher than 1999 a less sensitive range is selected.

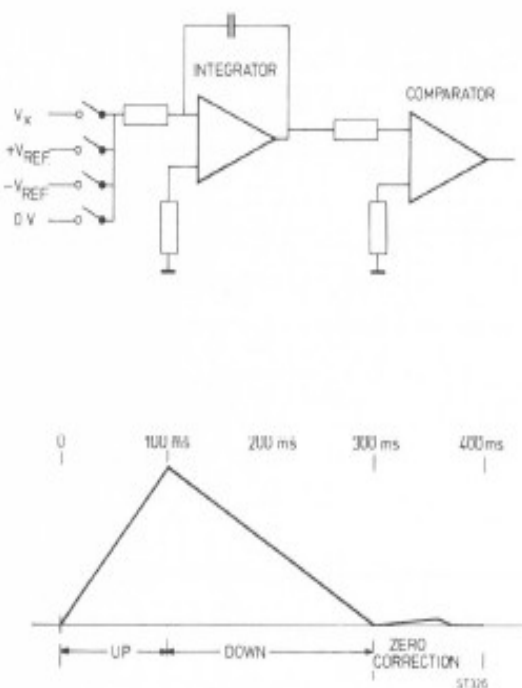


Fig. 3. Principle of the analog-to-digital converter

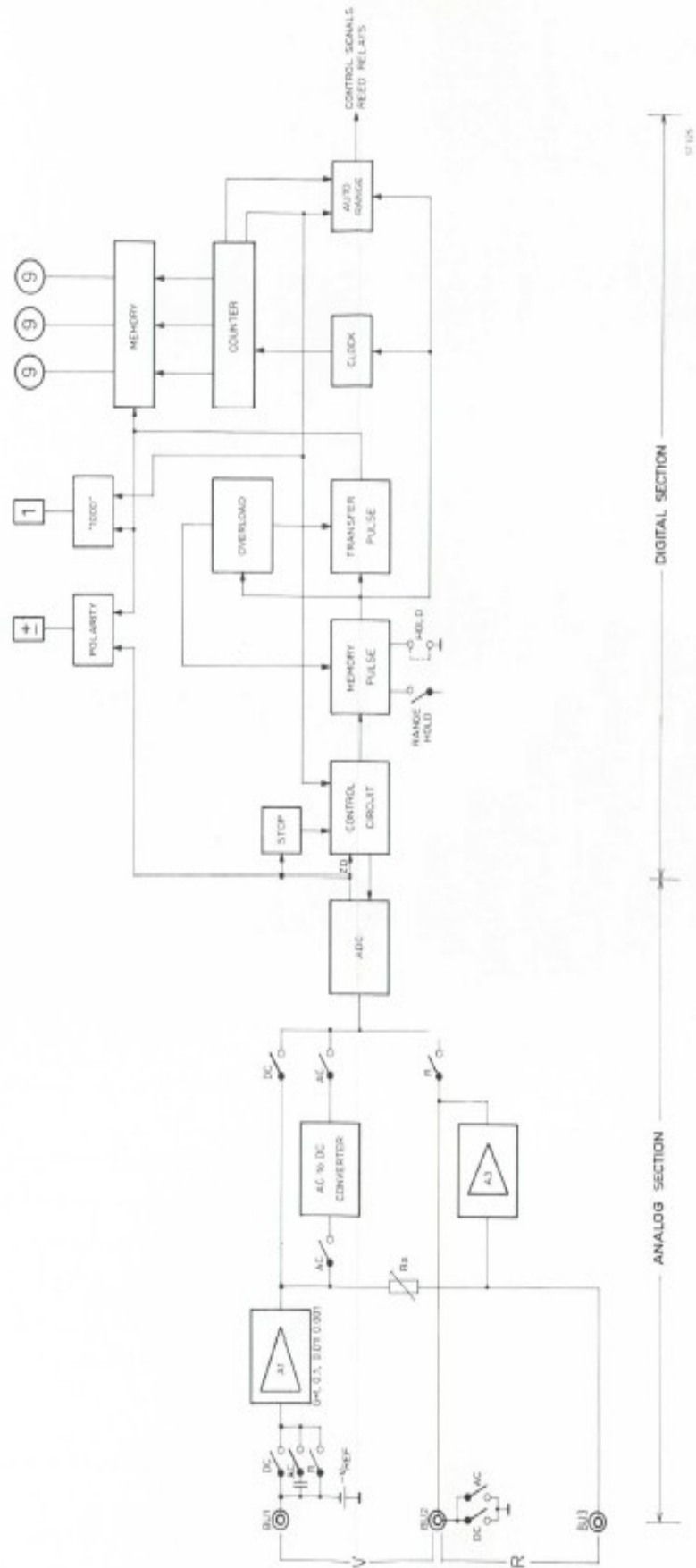


Fig. 2. Block diagram

## V. Operating instructions

## DIRECTIONS FOR USE

### A. SWITCHING-ON

- The instrument must be connected to a mains socket with rim earthing contacts by means of the 3-core mains lead.  
The housing of the instrument is then earthed via this lead.
- Set the power-switch to the ON-position. Use a mains-voltage of  $220 V_{\text{rms}} \pm 10\%$ , 50/60 Hz.

### B. MEASURING

1. Start the measurement by depressing one of the five mode selectors.

DC V : 1 mV up to 1000 V d.c.  
AC V : 1 mV up to 350 V a.c.  
M $\Omega$  : 0,001 M $\Omega$  up to 199,9 M $\Omega$   
k $\Omega$  : 0,001 k $\Omega$  up to 199,9 k $\Omega$   
 $\Omega$  : 0,1  $\Omega$  up to 199,9  $\Omega$

2. Direct voltages

- Depress button DC V.
- Connect the voltage source to be measured to terminals BU1 (red) and BU2 (black).

#### Notes

- BU1 (red) is "HIGH", BU2 (black) is "LOW"
- Maximum permissible input voltage : 1000 V
- Maximum permissible voltage between terminal BU2 (black) and chassis: 600 V
- The polarity at BU1 (red) with respect to BU2 (black) is indicated at the lefthand side of the figures.
- DC voltages from 1000 V up to 30 kV can be measured with EHT probe PM 9240 (see sub-point 3).

3. EHT voltages up to 30 kV with probe PM 9240

- Depress button DC V
- Connect the probe to the red and blue terminals of the instrument.
- Connect the earthing clip of the probe to a proper earthing point.

#### Notes

- Maximum permissible d.c. voltage on the probe is 30 kV
- To obtain a correct measurement consult the table below.

Input voltage	Range selected	Input resistance PM 2423 + PM 9240	Attenuation	Position dec. point	To obtain kV multiply with
<2 kV	2 V	991 M $\Omega$	91x	x,xxx	1
>2 kV, <20 kV	20 V	1000 M $\Omega$	100x	x,xx	10
>20 kV, <30 kV	200 V	1000 M $\Omega$	100x	xx,x	100

#### 4. Alternating voltages

- Depress button AC V
- Connect the voltage source to be measured to terminals BU1 (red) and BU2 (black).

##### Notes

- BU1 (red) is "HIGH", BU2 (black) is "LOW"
- Maximum permissible input voltage:  $350 V_{\text{rms}}$ .

#### 5. Resistances

- Depress button  $\Omega$ ,  $k\Omega$  or  $M\Omega$
- Connect the unknown resistance to BU2 (black) and BU3 (blue).

##### Notes

- Maximum permissible voltage at "R" input 100 V d.c. or a.c. for 10 secs. max.
- Test voltage: 2 V at range end.
- Test current:

Range	Current
200 $\Omega$	10 mA
2 $k\Omega$	1 mA
20 $k\Omega$	100 $\mu\text{A}$
200 $k\Omega$	10 $\mu\text{A}$
2 $M\Omega$	1 $\mu\text{A}$
20 $M\Omega$	100 nA
200 $M\Omega$	10 nA

#### C. POLARITY INDICATION

When button DC V is depressed to measure direct voltages, the polarity of the input signal is indicated at the lefthand side of the figures. The indicated polarity is present at terminal BU1 (red) with respect to terminal BU2 (black).

#### D. RANGE HOLD

When button RANGE-HOLD is in its horizontal position, the ranges are selected automatically.  
When button RANGE-HOLD is depressed the range selected just before depressing, is memorized.  
The position of the decimal point is fixed.

##### Example

The display is 15.15 V.

When after depressing button RANGE-HOLD the input signal becomes zero, the display is 00.00 V.

#### E. OVERRANGE INDICATION

Overrange is indicated by  $\text{\textcircled{R}}$  and continuous running of the display.

This will occur:

- When, in a fixed range, too a high input signal is applied to the instrument, button RANGE-HOLD being depressed.

In this case reset button RANGE-HOLD to its horizontal position.

- When the instrument is set to an unsuitable ohm-range.  
In this case select a suitable ohm-range.
- When no resistance is connected to the instrument on the resistance range.

#### F. HOLD

When the 2-pole connector at the rear of the instrument is shorted, the measured result just before shorting the connector is memorized.

The memorized display-results are maintained even if the input signal is changed.

#### Example

The display is 15.15 V.

When the input signal becomes zero the display will remain 15.15 V.

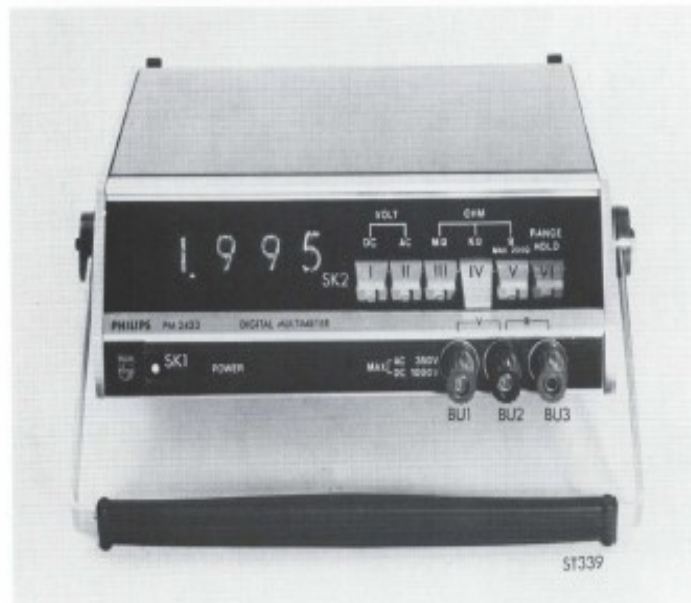


Fig. 4. Front view

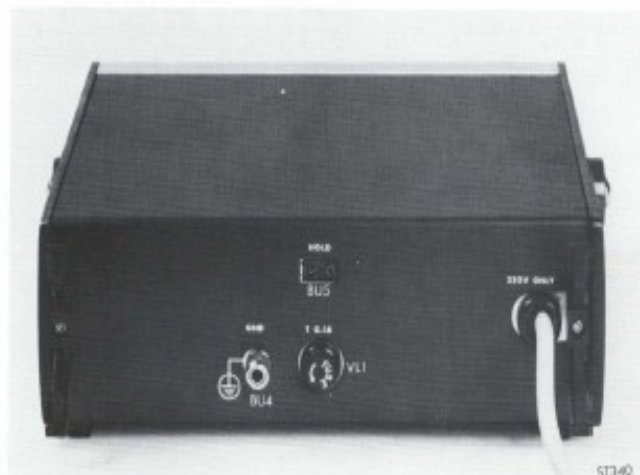


Fig. 5. Rear view

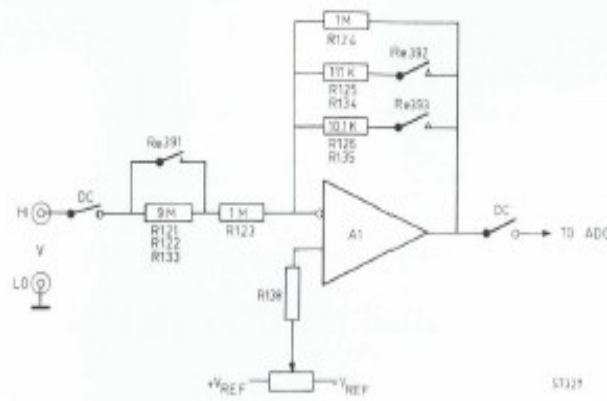


Fig. 6. Direct voltage measuring circuit

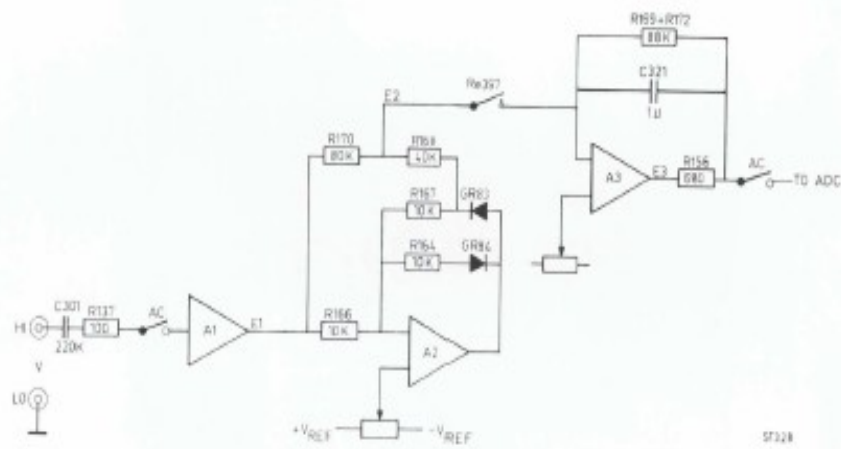


Fig. 7. Alternating voltage measuring circuit

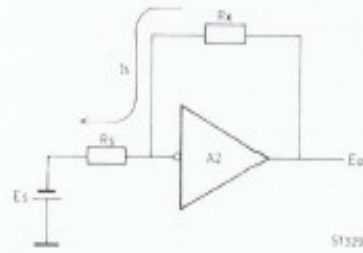


Fig. 8. Principle of resistance measurements

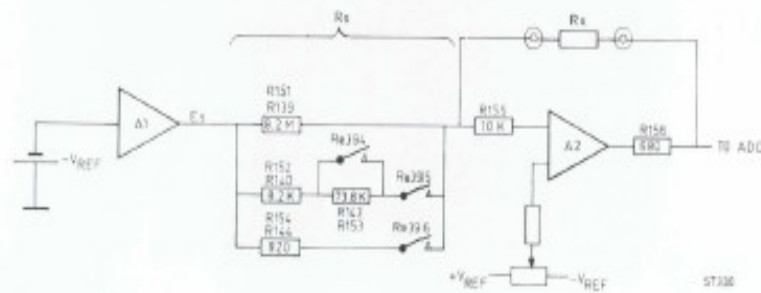


Fig. 9. Resistance measuring circuit

## VI. Circuit description

## SERVICE DATA

### A. ANALOG SECTION

#### 1. Direct voltage measurements (Fig. 6)

At direct voltage measurements, the input voltage is amplified by amplifier A1 to provide 2 V at full scale for all ranges. The obtained direct voltage is supplied to the analog-to-digital converter (ADC) via the push-button switch.

#### Survey

Range	Reed relay			Gain	Input impedance
	Re391	Re392	Re393		
1.999 V	energized			1	1 M $\Omega$
19.99 V				0.1	10 M $\Omega$
199.9 V		energized		0.01	10 M $\Omega$
1000 V			energized	0.001	10 M $\Omega$

Thus, the amplifier operates as an attenuator. Amplifier A1 consists of a difference amplifier with FET TS31 and an operational amplifier IC1.

#### 2. Alternating voltage measurements (Fig. 7)

The a. c. voltage supplied to the input is applied to amplifier A1 via a d. c. blocking capacitor. The unknown voltage is divided by 1, 10 and 100 by amplifier A1. The output voltage of A1 is applied to the a. c. -to-d. c. converter and rectified by rectifying amplifier A3. The output voltage of amplifier A3 is full-wave rectified by amplifier A2.

In the 350 V range the output voltage of A2 is divided by 10 by blocking TS33.

The obtained direct voltage is supplied to the analog-to-digital converter.

#### 3. Resistance measurements (Fig. 9)

Resistance measurements are based on varying the gain of an inverting amplifier by the unknown resistance as shown in Fig. 8.

Reference voltage  $-V_{\text{ref}}$  (-8.2 V) is amplified by amplifier A1.

Autoranging is performed by dividing the value of  $-V_{\text{ref}}$  by 1, 10 and 100 by means of reed-relays Re394, Re395 and Re396.

The gain is determined by  $\frac{R_x}{R_s}$ .

In the M $\Omega$ -range  $R_s = 8.2 \text{ M}\Omega$ , in the k $\Omega$  range  $R_s = 820 \text{ k}\Omega$  and in the  $\Omega$  range  $R_s = 820 \Omega$ .

In the 199.9 k $\Omega$  range, resistor  $R_s$  is changed due to accuracy relationship.

Resistance measuring amplifier A2 consists of TS32 and IC2. In the 200  $\Omega$  range the minimum resolution is 0.1  $\Omega$ .

## Survey

Function	Range	Gain	$E_g$	Reed relay			$R_g$	$I_g$	$E_o$
				A1	Re394	Re395			
$\Omega$	199.9 $\Omega$	1	8.2 V	-	-	energized	820 $\Omega$	10 mA	1,999 V
k $\Omega$	1.999 k $\Omega$	1	8.2 V	energized	energized	-	8.2 k $\Omega$	1 mA	
	19.99 k $\Omega$	0.1	0.82 V	energized	energized	-	8.2 k $\Omega$	100 $\mu$ A	
	199.9 k $\Omega$	0.1	0.82 V	energized	-	-	82 k $\Omega$	10 $\mu$ A	
M $\Omega$	1.999 M $\Omega$	1	8.2 V	-	-	-	8.2 M $\Omega$	1 $\mu$ A	
	19.99 M $\Omega$	0.1	0.82 V	-	-	-	8.2 M $\Omega$	0.1 $\mu$ A	
	199.9 M $\Omega$	0.01	82 mV	-	-	-	8.2 M $\Omega$	10 pA	

## 4. Survey analog section (Fig. 10)

Function	Range	Reed relay							TSS3
		Re391	Re392	Re393	Re394	Re395	Re396	Re397	
DC	1,999 V	l.c.							on
	19.99 V								on
	199.9 V		l.c.						on
	1000 V			l.c.					on
AC	1,999 V	l.c.						p.b.	on
	19.99 V	l.c.	l.c.					p.b.	on
	199.9 V	l.c.		l.c.				p.b.	on
	350 V	l.c.		l.c.				p.b.	off
$\Omega$	199.9 $\Omega$	l.c.						p.b.	on
k $\Omega$	1.999 k $\Omega$	l.c.			l.c.	p.b.			on
	19.99 k $\Omega$				l.c.	p.b.			on
	199.9 k $\Omega$					p.b.			on
M $\Omega$	1.999 M $\Omega$	l.c.							on
	19.99 M $\Omega$								on
	199.9 M $\Omega$		l.c.						on

- l.c. means that the corresponding reed-relay is activated by the logic circuit.

- p.b. means that the corresponding reed-relay is activated by the push-button switch.



5. Analog-to-digital converter (Fig. 11)

The d.c. signal delivered by the analog section (2 V at end-of-range) is converted into a "digital" form by means of an analog-to-digital converter.

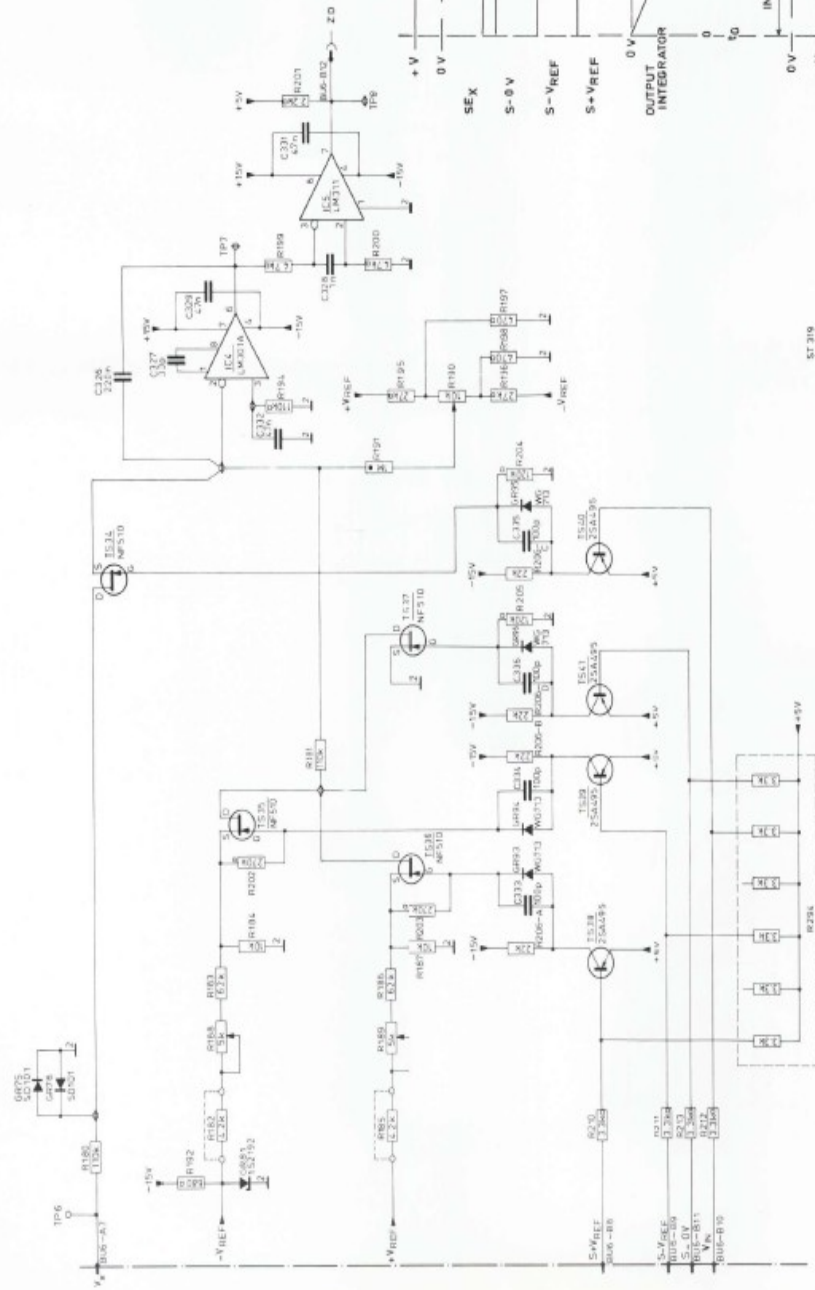
The conversion system used is conform to the dual slope principle.

The conversion time is 400 msec. and is divided in three periods as follows (see Fig. 12).

- up-integration from  $t_0$  up to  $t_1$  : 100 msec.
- down-integration from  $t_1$  up  $t_2$  : 200 msec. at end-of-range values.
- zero point correction time from  $t_2 - t_0$ .

The integrated value at end-of-range value (2 V input) is approximately 10 V.





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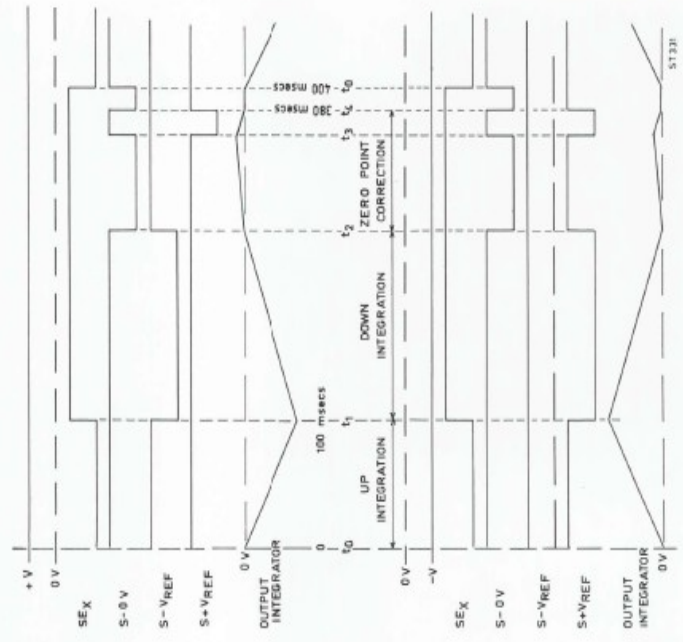


Fig. 12. Sequence diagram ADC

Fig. 11. Analog-to-digital converter

a. Up-integration ( $t_0 - t_1$ )

At the start of the up-integration ( $t_0$ ) TS34 is conductive and the voltage to be measured  $E_x$  (2 V at end-of-range) is supplied to the integrator. The FET's TS35, TS36 and TS37 are blocked.

The integrator output voltage decreases linearly with time. The time  $t_0$  up to  $t_1$  (100 msec.) is determined by counting 1000 clock pulses.

b. Down-integration ( $t_1 - t_2$ )

Upon completion of the up-integration ( $t_1$ ) TS34 is blocked and TS35 or TS36 becomes conductive thus supplying  $+V_{ref}$  or  $-V_{ref}$  to the integrator.

The down-integration is started.

As the reference voltage is of opposite polarity with respect to the input voltage the integrator output voltage goes to zero level.

The time  $t_1$  to  $t_2$  is 200 msec. at end-of-range values. The number of clock pulses counted during this time interval are displayed.

c. Zero point correction ( $t_2 - t_0$ )

Integrated circuits IC4 and IC5 are drifting.

So the comparator, IC5, does not compare the output voltage of the integrator with the real zero level, unless special measures are taken. To avoid this fault an automatic zero point correction is made by measuring again with respect to zero.

Upon completion of the down-integration ( $t_2$ ), which is determined at zero passage by a comparator, the zero level is connected to the integrator input via TS37. The drift-voltage is now measured by making an up-integration ( $t_2$  up to  $t_3$ ) and a down-integration ( $t_3$  up to  $t_4$ ).

At  $t_3$  (380 msec. after start) a reference voltage is supplied to the integrator in that way that the integrator output becomes again zero.

This zero level is reached at  $t = t_4$ .

In this way the zero drift of the integrator and the comparator is corrected automatically.

d. Rest time ( $t_4 - t_0$ )

If comparator zero is reached ( $t_4$ ) the reference voltage is disconnected from the integrator input and TS37 becomes conductive again up to  $t_0$ .

The controlling of FET switches TS34 up to TS37, and thus the timing of the conversion system, is effected by signals  $SE_x$ ,  $S+V_{REF}$ ,  $S-V_{REF}$  and  $S 0 V$ , which are formed in the digital section.

The field effect transistors (TS34-TS37) are conductive if their gate is about +5 V and is blocked if the gate is about -15 V.

## B. DIGITAL SECTION

### 1. Integrator input control circuit (Fig. 13)

The integrator input control circuit determines which voltage ( $E_x$ ,  $+V_{REF}$ ,  $-V_{REF}$  or circuit zero) is supplied to the integrator.

The functioning of this circuit can easily be derived from the sequence diagram Fig. 14.

The control circuit is controlled by:

- signal "1000" D, delivered by the counter (see B.7.)
- signal ZD (zero detect) from the analog-to-digital converter (see A.5.)
- signal STOP from the stop-pulse generator (see B.2.).

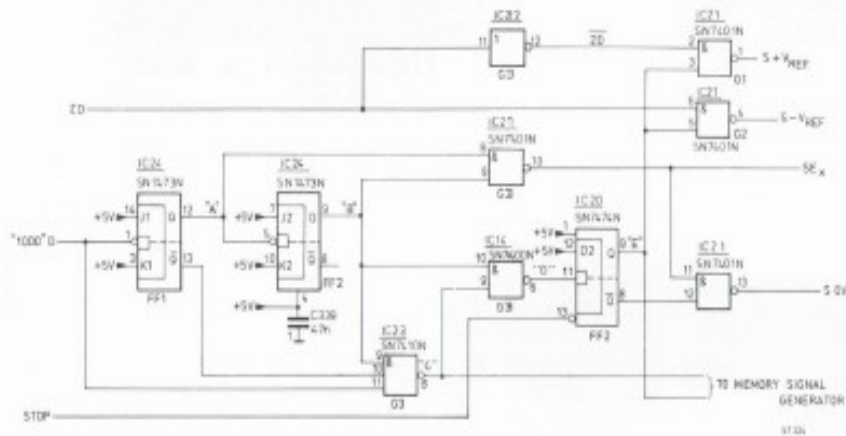


Fig. 13. Integrator input control circuit

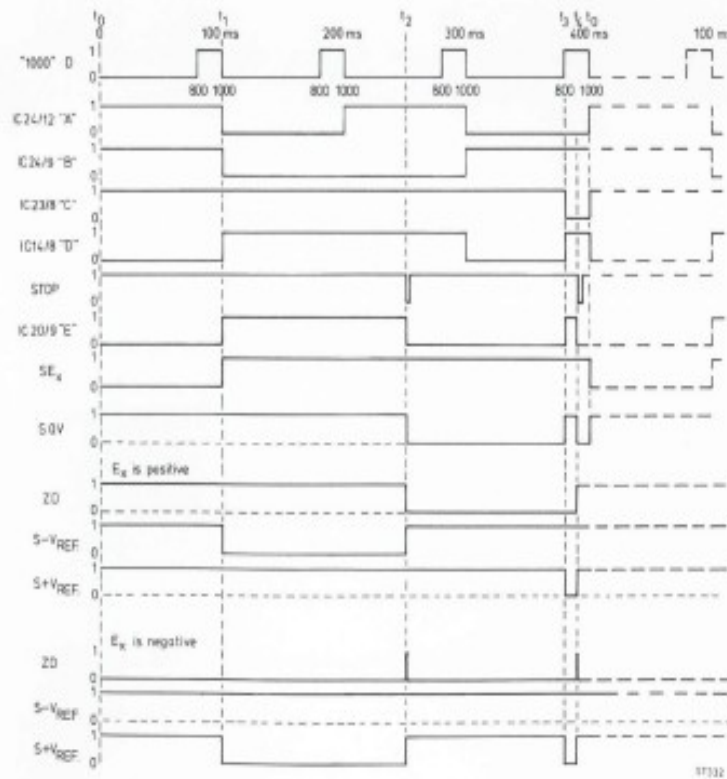


Fig. 14. Sequence diagram

## 2. Stop signal generator (Fig. 15)

When the output of the integrator passes zero, the comparator supplies a signal  $0\text{ V} \rightarrow 5\text{ V}$  or  $5\text{ V} \rightarrow 0\text{ V}$ . The signal  $0\text{ V} \rightarrow 5\text{ V}$  is fed to the base of TS44 via diode GR100, a signal  $5\text{ V} \rightarrow 0\text{ V}$  via diode GR99.

Transistor TS44 becomes conductive for a short period; thus the collector produces a zero level : the STOP signal.

Signal STOP is produced at  $t = t_2$  and  $t = t_4$ .

## 3. Memory signal generator

The memory signal is produced via the circuit of Fig. 16 and is formed by signals supplied by the integrator input control circuit (see B.1.).

The memory signal is always logical 1, except at completion of the down-integration ( $t_2$ ) a short delay of the negative-going edge supplied by IC22/2 ( $\bar{F}$ ) is achieved by capacitor C351.

The memory pulse may be inhibited by interconnecting the 2-pole connector HOLD (BU5) at the rear, so that input IC22/3 is connected to circuit zero and the display is hold.

The memory signal has the following 4 functions

- it is the clock signal for the autorange circuit
- it blocks the main gate
- it produces the transfer signal
- it is the clock signal for the OVER display

## 4. Transfer pulse and OVER display circuit (Fig. 18)

The transfer pulse and the OVER display are derived from the memory signal.

### a. Transfer-pulse

The transfer pulse is logical 0 as long as no memory signal is supplied and no overload condition is signalled (IC20/ $\bar{Q}$  is logical 0).

As soon as the memory signal becomes logical 0 at  $t = t_2$ , the transfer pulse becomes logical 1.

If an overload condition is signalled (IC20/Q becomes logical 1) the transfer-pulse is hold at a logical 1 level via transistor TS46.

### b. OVER display

OVER display is performed by not storing the display (the display is running) and by indication  $\textcircled{R}$ .

Overload means that the down-integration time ( $t_1$  up to  $t_2$ ) is longer than 200 msecs and that the memory signal is supplied after  $t_2 \geq 300$  msecs.

This results in a triggering of flip-flop IC20, as input D has become logical 1.

Thus : - Output IC20/Q becomes logical 1.

TS46 is turned on, which results in a continuous logical 1 for the transfer-pulse.

- Output IC20/ $\bar{Q}$  becomes logical 0.

TS50 is blocked, so that pilot lamp LA384 lights (OVER indication).

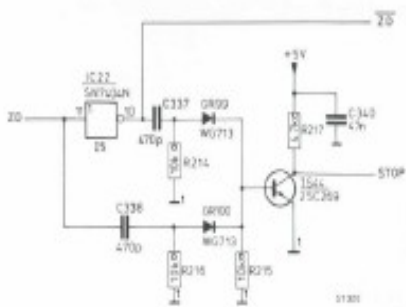


Fig. 15. Stop signal generator

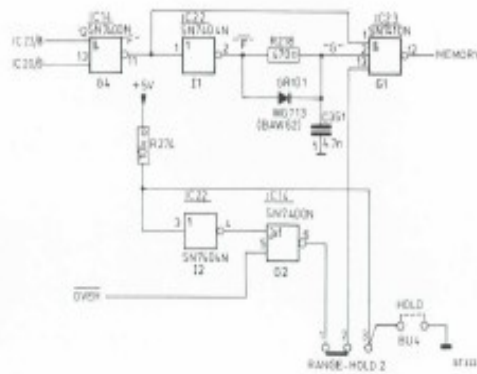


Fig. 16. Memory signal generator

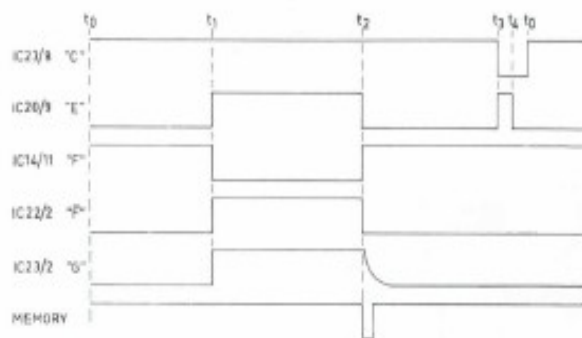


Fig. 17. Sequence diagram memory signal

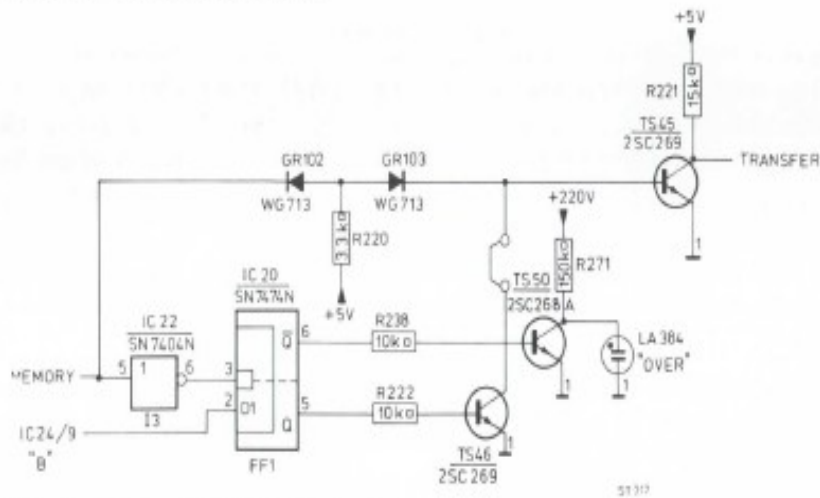


Fig. 18. Transfer pulse and OVER display circuit

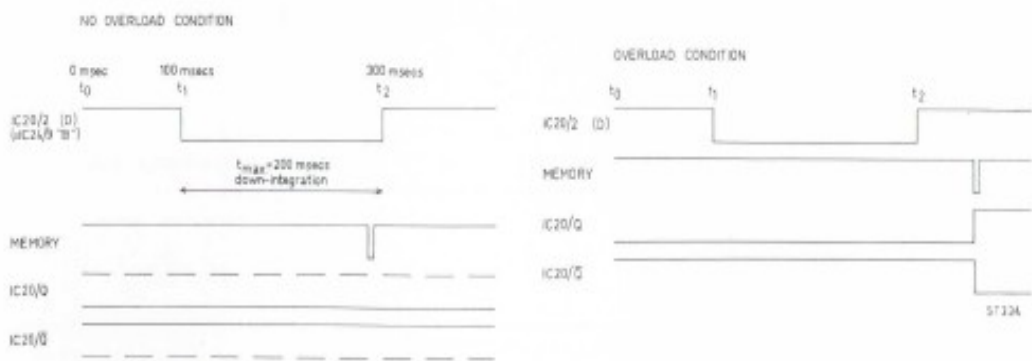


Fig. 19. Sequence diagram overload

### 5. Polarity indication

The polarity indication is determined by the circuit of Fig. 20.

It is memorized by flip-flop IC13/A upon completion of the up-integration by  $\overline{SE}_X$  and shifted into flip-flop IC13/B by the transfer signal.

#### Survey polarity indication

Polarity of input signal	$\overline{ZD}$	IC13/A/8	IC13/B/15	IC13/B/14	TS48	TS49	
		after $\overline{SE}_X$ transition	after transfer signal				
positive	1	0	0	1	blocked	on	LA382 lights (+ indication)
negative	0	1	1	0	on	blocked	LA383 lights (- indication)

The polarity indication is only operating in the case of DC voltage measurements. If button DC is not depressed the bases of TS48 and TS49 are kept at a positive level via R223 and R224.

### 6. Clock oscillator

The clock oscillator is an astable multivibrator with a frequency of 10 kHz, which can be adjusted by means of potentiometer R232.

### 7. Display

The counter consists of three decade counters IC15 ... IC17 for the units, tens and hundreds. The clock pulses are applied to the counter input via main-gate IC23, which can be blocked by applying a logical 0 at inputs 4 and 5.

The thousand figure is indicated by pilot lamp LA381 which is controlled in accordance with Fig. 21.

From output IC16/11 and IC17/1+12 signals are taken to control the automatic range selector and from output IC17/11 a signal to control the integrator input control circuit.

Storing of the information takes place in three Quadruple bistable latches (IC9...IC11). During the transfer pulse the information at the outputs of the decade counters is transferred to the latches.

The measured value is memorized until a new transfer pulse is supplied.

The output signals of the memory (IC9...IC11), which are available in 1-2-4-8 code are decoded by decoder/drivers IC6...IC8, thus converting the measured value into a decimal information which is indicated by indicators B371...B373.

### 8. Autorange circuit

Fig. 22 represents a simplified block diagram of the autorange circuit.

#### a. Up-down counter

The up-down counter consists of two J-K flip-flops (IC26).

If signal "UP" or signal "DOWN" is logical 1, the outputs of both flip-flops will change state when a clock signal is applied to the clock terminal.



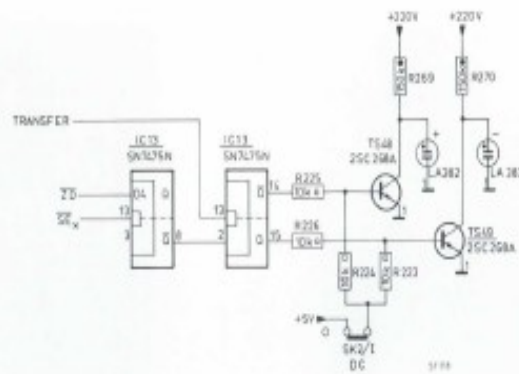


Fig. 20. Polarity indication circuit

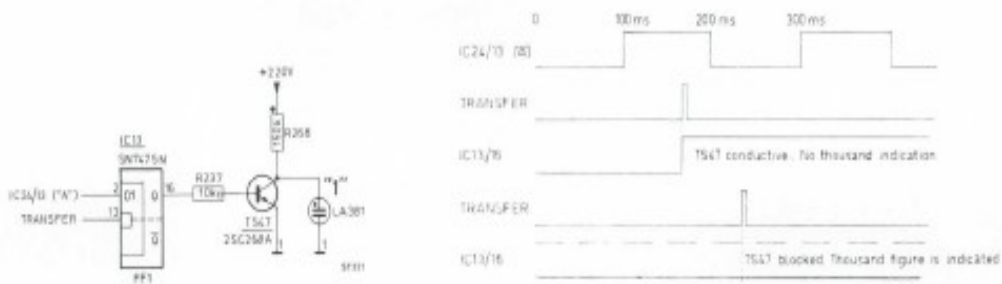


Fig. 21. Thousands indication

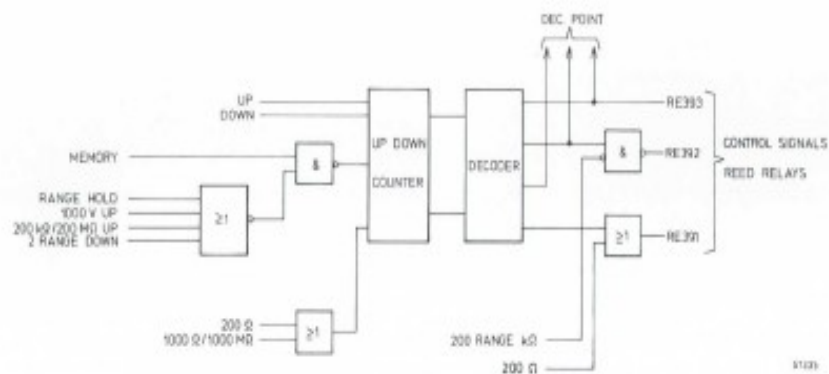


Fig. 22. Block diagram autorange circuit

Truth table up-down counter

Range	Output IC26			
	-/12	-/13	-/9	-/8
2	0	1	1	0
20	1	0	1	0
200	0	1	0	1
1000	1	0	0	1

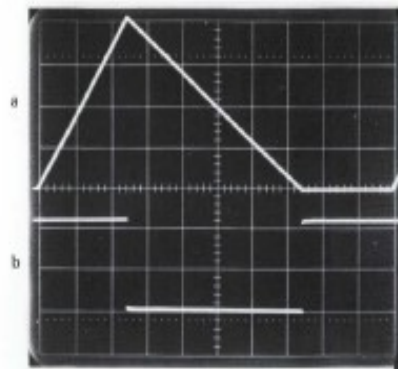


Fig. 23. Up signal ST350A

Oscilloscope settings

Time base: 20 msec/cm, a. Integrator output (TP7)  
calibrated at 40 msec/cm b. UP pulse (IC18/4)  
Amplitude: 2 V/cm

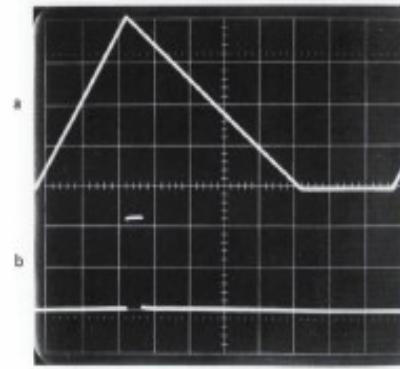


Fig. 24. Down signal ST350B

Oscilloscope settings

Time base: 20 msec/cm, a. Integrator output (TP7)  
calibrated at 40 msec/cm b. DOWN pulse (IC18/2)  
Amplitude: 2 V/cm

b. UP signal

The "UP" signal is derived from the counter via flip-flops IC19/FF1 and -/FF2 and corresponds with the inverted signal "B", (Fig. 14).

c. DOWN signal (Fig. 15)

The down signal is produced from output "D" of the  $10^1$  decade counter, output "A" of the  $10^2$  decade counter and the inverted signal "B" of Fig. 14.

The "DOWN" signal is only logical 1 to counter position 000 - 0180 (thus 18 msec) during down-integration ( $t_1 = 100$  msec.)

d. Inhibit of UP or DOWN ranging

If the autorange circuit is set to the highest or lowest possible range of a certain measuring mode, the up-down counter is not able to select a higher or a lower range.

Inhibiting "UP" or "DOWN" ranging is effected by making the clock input of the counter logical 0 via Nand gates IC19/G1, IC27/G1, IC27/G3 or by depressing button RANGE HOLD. The memory signal is then blocked and no range switching is effected.

The next inhibits are performed:

- RANGE HOLD: by depressing button RANGE HOLD the collector of T855 becomes logical 0.
- Range 1000 UP (1000 V d.c. or 350 V a.c.): Nand-gate IC27/G1 supplies a logical 0.
- Range 200 k $\Omega$  and range 200 M $\Omega$  UP: Nand-gate IC27/G3 supplies a logical 0.
- Range 2 DOWN: Nand-gate IC19/G1 supplies a logical 0.

e. Fixed range

If button  $\Omega$  is depressed the reset inputs of both flip-flops IC26/FF1 and -/FF2 are logical 0. Both flip-flops are reset, thus the outputs are set to the lowest range.

If now ranges 1000 k $\Omega$  or 1000 M $\Omega$  are selected both flip-flops are reset via Nand-gate IC29/G1 and the 200 k $\Omega$  or 200 M $\Omega$  range is set.

f. Decoder

The outputs of the up-down counter are decoded into signals to drive the reed-relays in the input circuit of the analog section.

Furthermore, the control signals for the decimal point are provided by the decoder.

### C. POWER SUPPLY

The power supply provides the voltages for the analog and digital sections. The voltages are:

+15 V and -15 V for the amplifier circuits

+5 V for the digital section

The +15 V, -15 V and +5 V are stabilized; the 220 V is a non-stabilized supply voltage.

The reference voltage  $+V_{REF}$  is derived from the +15 V circuit.

## VII. Gaining access to and replacing of parts

USE A WELL-FITTING CROSS-HEADED SCREW-DRIVER TO DEMOUNT THE INSTRUMENT TO PREVENT DAMAGING THE CROSS-SLOTTED SCREWS.

### A. REMOVING THE CABINET

- Remove both screws "A" (Fig. 33)
- Slide the cabinet from the unit

### B. PRINTED WIRING BOARDS

#### 1. Analog section U1

- Remove both screws "B" (Fig. 34)
- Remove the screening cap
- Unplug CS1 and CS2
- Slide board U1 out of connector BU6
- Remove board U1.

#### 2. Digital section U2

- Remove board U1
- Unsolder the wires at the wiring side of the printed wiring board of U1
- Remove both screws "C" (Fig. 35)
- Remove both screws "D" (Fig. 35)
- Slide printed wiring board U2 out of connector BU7.

## C. SEMICONDUCTORS

1. Integrated circuits IC1 and IC2 (ordering number 5322 209 84214) are selected integrated circuits, type LM301A.

This IC has been selected following the specifications mentioned below (see Fig. 25).

- Temperature coefficient of input offset voltage  $< \pm 5 \mu\text{V}/^\circ\text{C}$ .
- Output voltage at  $0^\circ\text{C}$ ,  $+30^\circ\text{C}$  and  $+60^\circ\text{C}$  should be  $\pm 300 \text{ mV}$  maximum and  $< 500 \mu\text{V}/^\circ\text{C}$ .

2. Integrated circuit IC3 (ordering number 5322 209 84215) is a selected integrated circuit, type LM301A.

This IC has been selected following the specifications mentioned below (see Fig. 25).

- Output voltage should be  $\pm 300 \text{ mV}$  maximum and  $< 1 \text{ mV}/^\circ\text{C}$ .

3. Integrated circuit IC4 (ordering number 5322 209 84216) is a selected integrated circuit, type LM301A.

This IC has been selected following the specifications mentioned below (see Fig. 26).

- Temperature coefficient of output voltage  $< \pm 100 \mu\text{V}/^\circ\text{C}$ .
- Output voltage at  $0^\circ\text{C}$ ,  $+30^\circ\text{C}$  and  $+60^\circ\text{C}$  should be  $\pm 30 \text{ mV}$  maximum and  $< \pm 100 \mu\text{V}/^\circ\text{C}$ .

4. Diodes GR71 and GR72 (ordering number 5322 130 34175), type IS1516, are lackered black to avoid light effects.

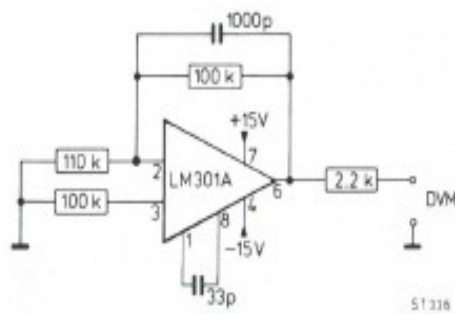


Fig. 25. Circuit for selection of IC1, IC2 and IC3

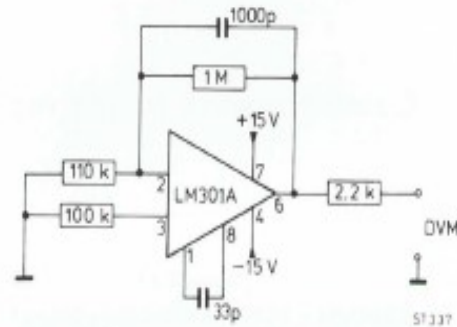


Fig. 26. Circuit for selection of IC4

## VIII. Maintenance and service

Digital multimeter PM 2423 requires no maintenance because the instrument contains no components which are subject to wear.

However, to ensure reliable and faultless operation, the instrument should not be exposed to moisture, heat, corrosive vapours and excessive dust.

### Service hints

If service work is to be carried out the following points should be taken into account to avoid damage to the instrument.

- In case of measurements on a switched-on instrument proceed carefully to avoid short-circuits by means of measuring clips or measuring hooks.
- For soldering use absolutely acid-free solder.
- The IC-circuits are especially sensitive to negative potentials, which will damage the IC.
- For all soldering work on the printed circuit boards use a miniature soldering iron (35 W max.) with a tin-cleaner or a vacuum soldering iron.

In the next table the most important voltages at the test points are given.

General conditions:

- nominal mains voltage (220 V, 50 Hz)
- calibrated
- voltages with respect to circuit zero (black terminal), unless otherwise stated
- the data given represent the properties of an average instrument and may serve as a guide only.
- voltages are measured with digital multimeter PM 2422.
- oscillograms are made with oscilloscope PM 3250.

Test point	Fig.	Function	Condition	Value	Remark
G1	28	Circuit zero of the digital section ( $\perp^1$ )		0 V	} G1, G2 and G3 are inter-connected
G2	27	Circuit zero of the analog-to-digital converter ( $\perp^2$ )		0 V	
G3	27	Circuit zero of the analog input circuit ( $\perp^3$ )		0 V	
+220 V	28	Supply voltage indicators		+208 V	
+15 V	27 } 28 }	Supply voltage amplifiers		+14,45 V	
-15 V	27 } 28 }	Supply voltage amplifiers		-14,4 V	
+5 V	27 } 28 }	Supply voltage integrated circuits		+5,15 V	
+V <sub>REF</sub>	28	Reference voltage ADC		+8,61 V	

Test point	Fig.	Function	Condition	Value	Remarks		
TP3	27	Output amplifier A1	- Button DC V depressed and supply respectively to input	1 V d.c.	-1 V d.c.		
			10 V d.c.				
			100 V d.c.				
			1000 V d.c.				
			- Button AC V depressed and supply respectively to input	1 V a.c.	1 V a.c.		see also oscillogram Fig. 29.1.a.
			10 V a.c.				
			100 V a.c.				
			220 V a.c.	2.2 V a.c.			
			- Button M $\Omega$ depressed and connect respectively to R input	1 M $\Omega$	+870 mV		
			10 M $\Omega$	+870 mV			
			100 M $\Omega$	+88.5 mV			
			- Button k $\Omega$ depressed and connect respectively to R input	1 k $\Omega$	+8.86 V		
			10 k $\Omega$	+870 mV			
			100 k $\Omega$	+870 mV			
- Button $\Omega$ depressed and connect 100 $\Omega$ to R input		+8.86 V					
TP4	27	Output amplifier A2	- Button AC V depressed and supply respectively to input	1 V a.c.	-0.83 V d.c.		
			10 V a.c.				
			100 V a.c.				
			220 V a.c.	-2.04 V d.c.			
			- Button M $\Omega$ depressed and connect respectively to R input	1 M $\Omega$	-1 V d.c.		
			10 M $\Omega$				
			100 M $\Omega$				

Test point	Fig.	Function	Condition	Value	Remark
			- Button k $\Omega$ depressed and connect respectively to R input		
			1 k $\Omega$		
			10 k $\Omega$	-1 V d.c.	
			100 k $\Omega$		
			- Button $\Omega$ depressed and connect 100 $\Omega$ to R input	-1 V d.c.	
TP5	27	Output amplifier A3 (a.c. -to -d.c. converter)	- Button AC V depressed and supply respectively to input		
			1 V	} +0.456 V d.c.	see also oscillogram Fig. 29.I.b.
			10 V		
			100 V		
			220 V	+0.98 V d.c.	
TP6	27	Input analog-to-digital converter	- Button DC V depressed and supply respectively to input		
			1 V d.c.	} -0.83 V	
			10 V d.c.		
			100 V d.c.		
			1000 V d.c.		
			- Button AC V depressed and supply respectively to input		
			1 V a.c.	} -1 V	
			10 V a.c.		
			100 V a.c.		
			220 V a.c.	-0.22 V	
			- Button M $\Omega$ depressed and connect respectively to R input		
			1 M $\Omega$	} -1 V	
			10 M $\Omega$		
			100 M $\Omega$		
			- Button k $\Omega$ depressed and connect respectively to R input		
			1 k $\Omega$	} -1 V	
			10 k $\Omega$		
			100 k $\Omega$		
			- Button $\Omega$ depressed and 100 $\Omega$ connected to R input	-1 V	

Test point	Fig.	Function	Condition	Value	Remark
TP7	27	Integrator output	+1 V supplied to the input (+ at BU1)		see oscillogram Fig. 29.II.a.
			-1 V supplied to the input (+ at BU2)		see oscillogram Fig. 29.III.a.
TP8	27	Output comparator (signal ZD)	+1 V supplied to the input (+ at BU1)		see oscillogram Fig. 29.II.b.
			-1 V supplied to the input (+ at BU2)		see oscillogram Fig. 29.III.b.
TP10	28	Control signal "E" of the input control circuit	-1.999 V d.c. supplied to the input (+ at BU2)		see oscillogram Fig. 29.IV.b.
TP11	28	Memory signal			see oscillogram Fig. 29.V. and VIII.a.
TP12	28	Output clock oscillator			see oscillogram Fig. 29.VI.
collector TS43	28	Clock oscillator			see oscillogram Fig. 29.VII.
collector TS45	28	Transfer pulse			see oscillogram Fig. 29.VIII.b.
IC21/1	28	Control signal $S+V_{REF}$	at end-of-range values		see oscillogram Fig. 29.IX.
IC21/10	28	Control signal $SE_x$	at end-of-range values		see oscillogram Fig. 29.X.
IC21/13	28	S 0 V	at end-of-range values		see oscillogram Fig. 29.XI.
IC24/12	28	Control signal			see oscillogram Fig. 29.XII.



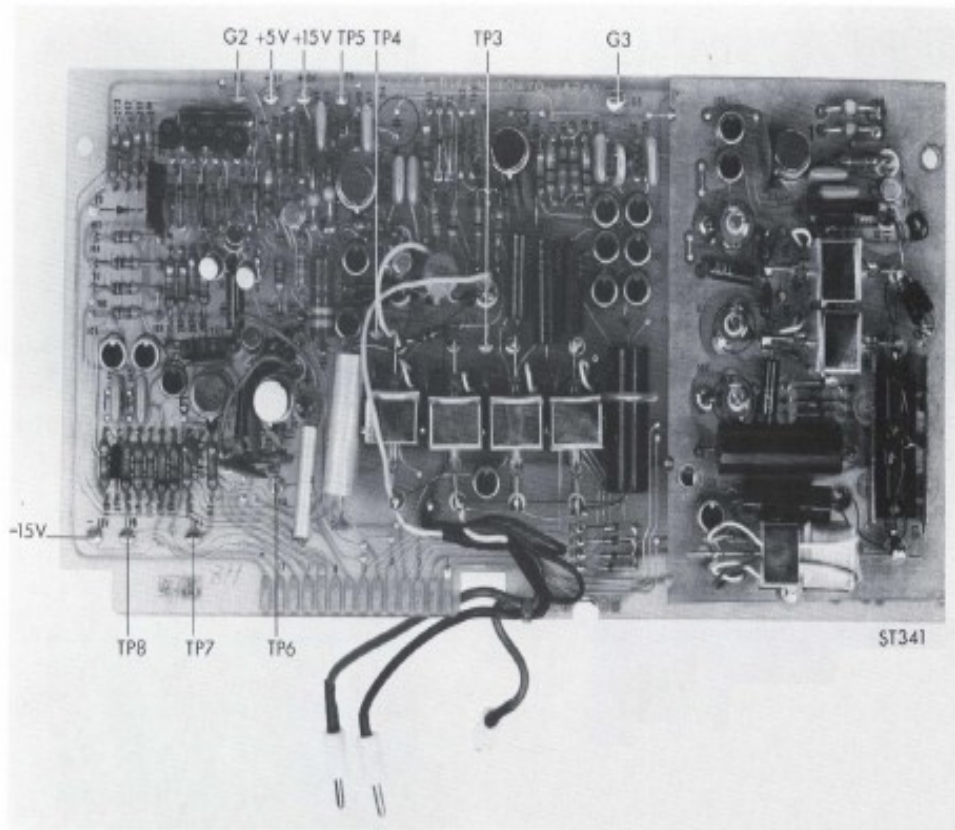


Fig. 27. Test points on unit U1

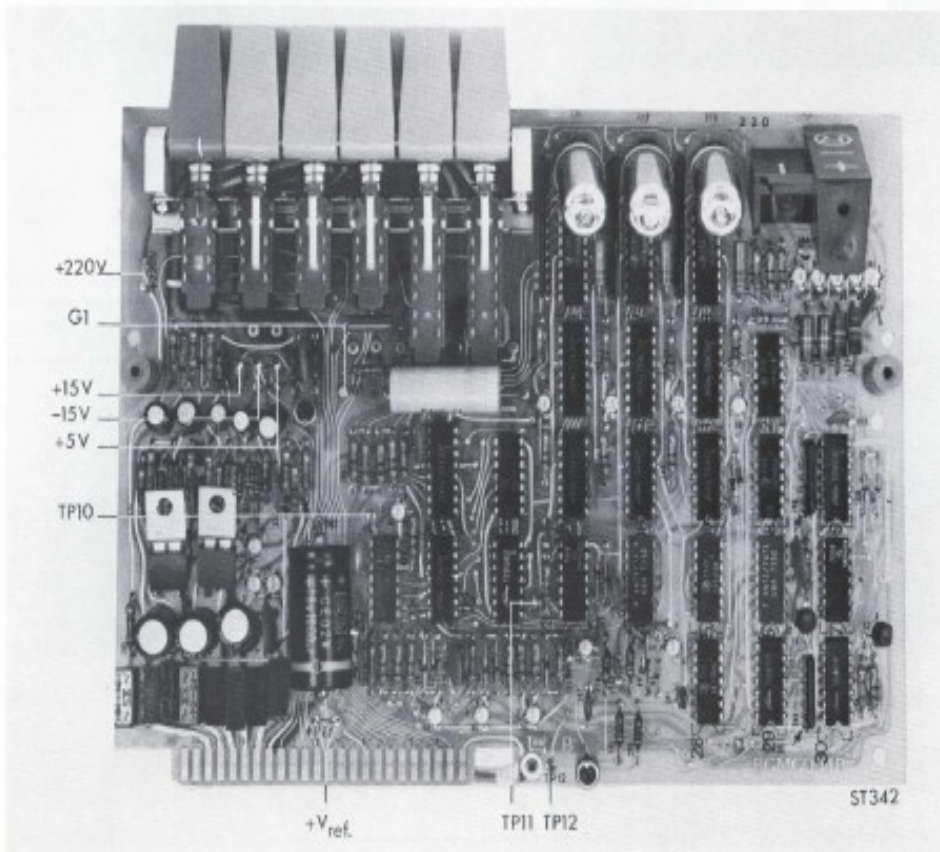


Fig. 28. Test points on unit U2

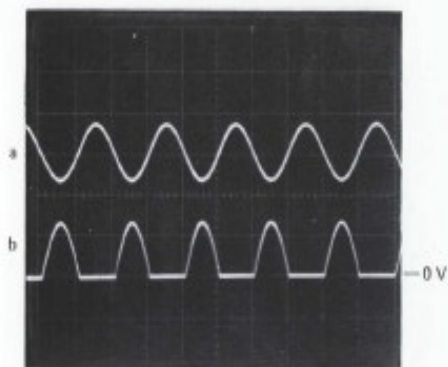


Fig. 29. Ia+b

ST351A

Oscilloscope settings

Time base: 20 msec/cm,  
calibrated at 40 msec/cm  
Amplitude: 1 V/cm

Input voltage

Amplitude: 1 V<sub>rms</sub>  
Frequency: 1 kHz

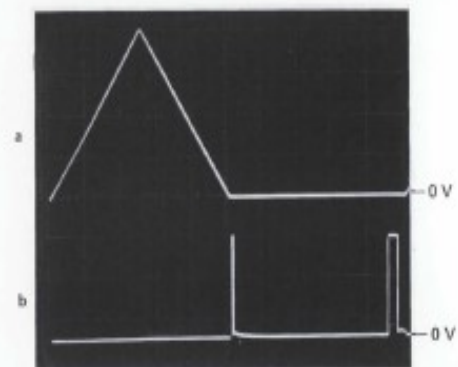


Fig. 29. IIa+b

ST351B

Oscilloscope settings

Time base: 0.5 msec/cm  
Amplitude: 2 V/cm for  
signal a, 1V/cm for signal b.

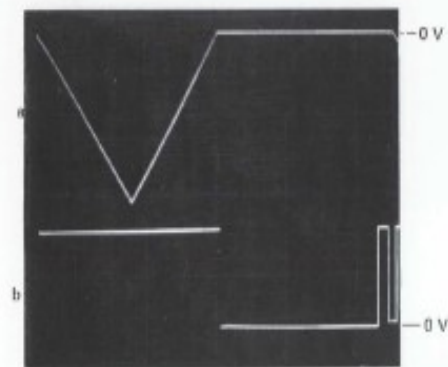


Fig. 29. IIIa+b

ST352A

Oscilloscope settings

Time base: 20 msec/cm,  
calibrated at 40 msec/cm  
Amplitude: 1 V/cm

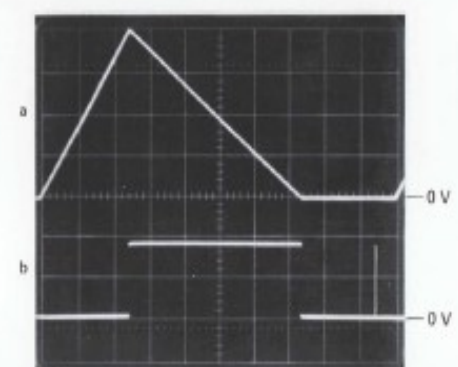


Fig. 29. IVa+b

ST352B

Oscilloscope settings

Time base: 20 msec/cm,  
calibrated at 40 msec/cm  
Amplitude: 2 V/cm

Remark

Signal a is the integrator  
output signal available at  
TP7.

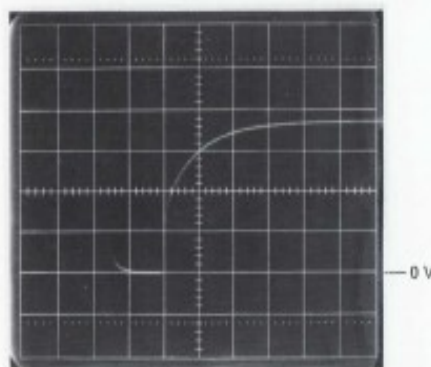


Fig. 29. V.

ST353A

Oscilloscope settings

Time base: 2 μsec/cm,  
calibrated at 4 μsec/cm  
Amplitude: 1 V/cm

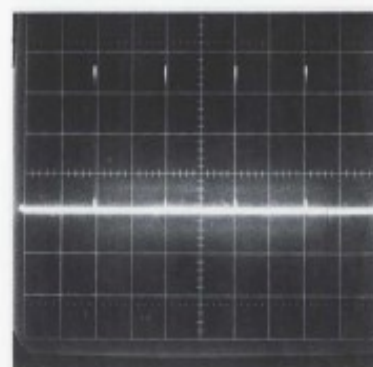


Fig. 29. VI.

ST353B

Oscilloscope settings

Time base: 50 μsec/cm  
Amplitude: 1 V/cm

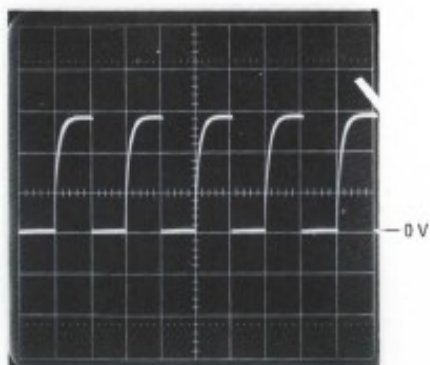


Fig. 29.VII.

ST354A

Oscilloscope settings

Time base: 50  $\mu$ sec/cm  
Amplitude: 5 V/cm

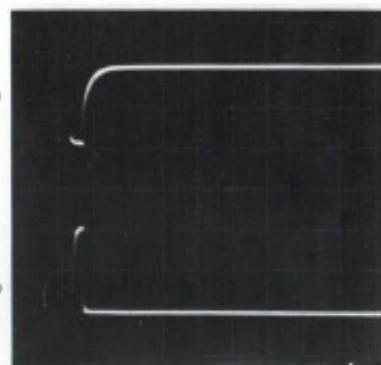


Fig. 29.VIII.

ST354B

Oscilloscope settings

Time base: 10  $\mu$ sec/cm  
Amplitude: 2 V/cm

a. Memory signal  
b. Transfer signal

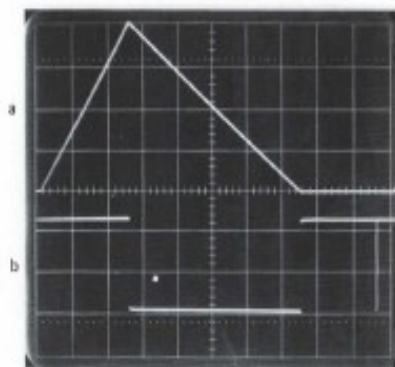


Fig. 29.IX.

ST355A

Oscilloscope settings

Time base: 20 msec/cm, calibrated at 40 msec/cm  
Amplitude: 2 V/cm

a. Integrator output (TP7)  
b.  $S + V_{REF}$

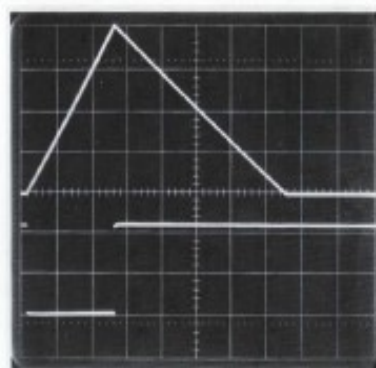


Fig. 29.X.

ST355B

Oscilloscope settings

Time base: 20 msec/cm, calibrated at 40 msec/cm  
Amplitude: 2 V/cm

a. Integrator output (TP7)  
b.  $SE_X$

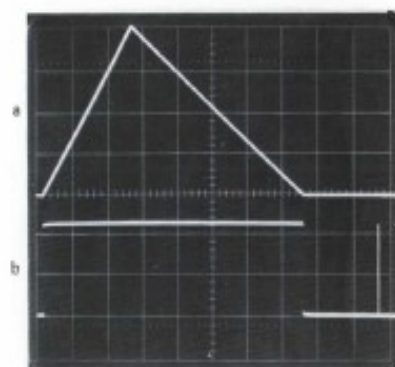


Fig. 29.XI.

ST356A

Oscilloscope settings

Time Base: 20 msec/cm, calibrated at 40 msec/cm  
Amplitude: 2 V/cm

a. Integrator output (TP7)  
b.  $S + 0 V$

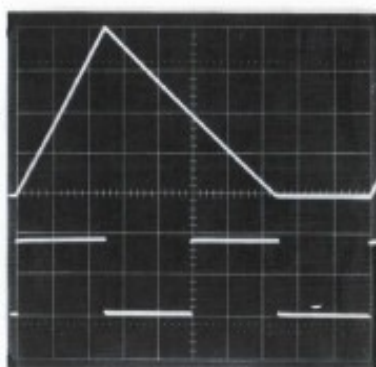


Fig. 29.XII.

ST356B

Oscilloscope settings

Time base: 20 msec/cm, calibrated at 40 msec/cm  
Amplitude: 2 V/cm

a. Integrator output (TP7)  
b. Signal at IC24/12

## IX. Survey of adjustments

The next table gives a survey of the adjustments of multimeter PM 2423.  
For a complete adjustment adhere to the sequence of chapter X.

Control element	Fig.	Checking or adjusting point	According to chapter
		<u>Supply voltages</u>	X. A
		+15 V, -15 V, +220 V	
R291	-	+5 V	
R232	30	<u>Clock oscillator frequency</u>	X. B
		<u>Zero point settings</u>	X. C
R136	30	DC zero	
R171	30	AC zero	
R174	30	$\Omega$ zero	
R190	30	Digital zero	
		<u>DC ranges</u>	X. D
R189	30	+1.999 V range	
R188	30	-1.999 V range	
R133	30	+19.99 V range	
R134	30	+199.9 V range	
R135	30	+1000 V range	
		<u>AC ranges</u>	X. E
R172	30	1.999 V range, 400 Hz	
R173	30	350 V range, 400 Hz	
C309	30	1.999 V range, 20 kHz	
C310	30	19.99 V range, 20 kHz	
C311	30	199.9 V range, 20 kHz	
		<u>Resistance ranges</u>	X. F
R151	30	1.999 M $\Omega$ range	
R152	30	1.999 k $\Omega$ range	
R153	30	19.99 k $\Omega$ range	
R154	30	199.9 $\Omega$ range	

### Required test equipment

To calibrate this measuring instrument only reference voltages and measuring equipment with the required accuracy should be applied.

The test equipment and standards required for checking, adjusting or calibration are listed in the next table.

Test	Required performance	Recommended equipment	
Zero point setting	Sensitivity 160 $\mu$ V	DC Voltmeter e.g. PM 2434 or PM 2435	
DC Calibration	Accuracy : at least $\pm 0.03$ %	DC Standard e.g. Fluke 332A	
	Range : 1 V up to 1000 V		
AC Calibration	Accuracy : at least $\pm 0.1$ %	AC Standard e.g. Hewlett Packard HP742A	
	Voltage : 1 V up to 350 V <sub>RMS</sub>		
	Frequency : 40 Hz up to 20 kHz		
Resistance calibration	Resistance	Accuracy	Standard series resistors (e.g. metal film 0.1 %) <u>or</u> Guildline type 9330
	19 $\Omega$	at least 1.0 %	
	190 $\Omega$	at least 0.1 %	
	1.9 k $\Omega$	at least 0.1 %	
	19 k $\Omega$	at least 0.1 %	
	190 k $\Omega$	at least 0.1 %	
	1.9 M $\Omega$	at least 0.2 %	
	19 M $\Omega$	at least 0.5 %	

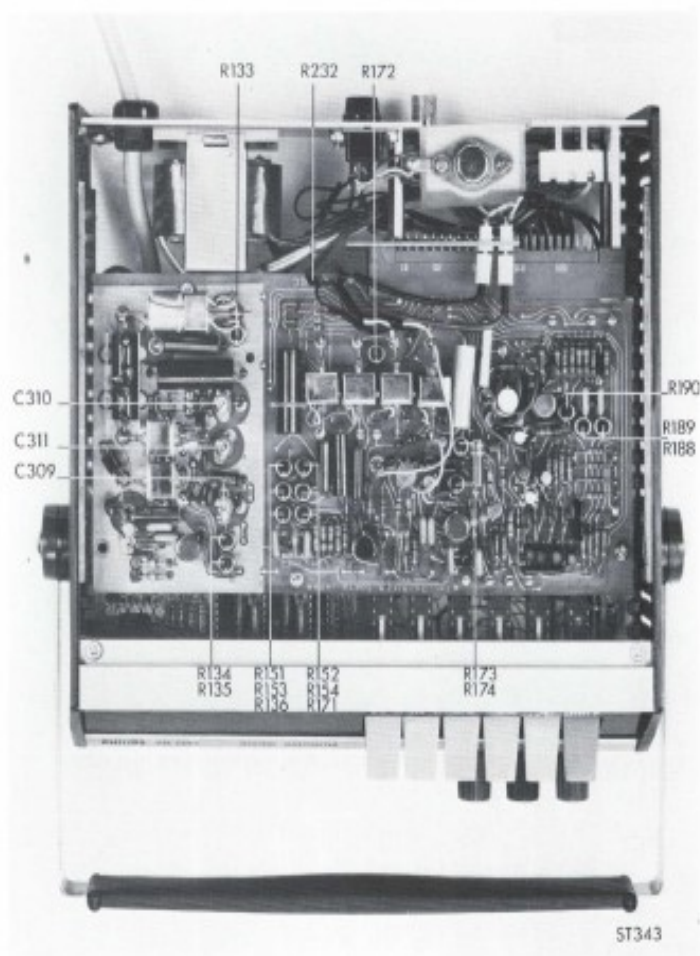


Fig. 30. Indications of adjusting elements

## X. Checking and adjusting

The tolerances stated in this chapter correspond to the factory data, which only apply to a newly adjusted instrument.

A survey of the adjustments and adjusting elements is given in chapter IX.

When individual components, especially semi-conductors, are replaced, the relevant section should be completely readjusted.

Perform calibration after a warm-up time of approximately 20 minutes.

### A. SUPPLY

#### 1. Current consumption

The current consumption from the mains should be about 75 mA.

#### 2. +15 V supply

The output voltage of the +15 V supply source should be  $+15\text{ V} \pm 10\%$ .

Measuring points : jumper +15 V on board U2 (+) and BU2 (0 V; black terminal).

#### 3. -15 V supply

The output voltage of the -15 V supply source should be  $-15\text{ V} \pm 10\%$ .

Measuring points : jumper -15 V on board U2 (-) and BU2 (0 V; black terminal).

#### 4. +V<sub>REF</sub> supply

The reference voltage +V<sub>REF</sub> should be  $8.61\text{ V} \pm 5\%$ .

Measuring points : jumper +REF on board U2 (+) and BU2 (0 V; black terminal).

#### 5. +220 V supply

The output voltage of the +220 V supply source should be  $+220\text{ V} \pm 10\%$ .

Measuring points : jumper +220 V on board U2 (+) and BU2 (0 V; black terminal).

#### 6. +5 V supply

The output voltage of the +5 V supply source should be  $+5\text{ V} \pm 0.1\text{ V}$ .

If necessary, adjust with potentiometer R291.

Measuring points : jumper +5 V on board U2 (+) and BU2 (0 V; black terminal).

### B. CLOCK OSCILLATOR FREQUENCY

The output frequency of the clock oscillator should be  $10\text{ kHz} \pm 3\text{ Hz}$ .

If necessary, readjust with potentiometer R232.

Measuring points : TP12 and BU2 (0 V).

### C. ZERO POINT SETTINGS

#### 1. DC zero

- Depress button DC V (SK2/I).

- Connect a d.c. voltmeter to test points TP6 and G2.

- The indication on the d.c. voltmeter should be  $0 \pm 100\ \mu\text{V}$ .

- If necessary, adjust with potentiometer R136.

## 2. AC zero

- Depress button AC V (SK2/II).
- Connect a d.c. voltmeter to test points TP6 and G2.
- Short circuit terminals BU1 and BU2.
- The indication on the d.c. voltmeter should be  $0 \pm 100 \mu\text{V}$ .
- If necessary, adjust with potentiometer R171.

## 3. $\Omega$ -Zero

- Depress button  $\Omega$  (SK2/V).
- Connect a d.c. voltmeter to test points TP6 and G2.
- Short-circuit terminals BU2 and BU3.
- The indication on the d.c. voltmeter should be  $0 \pm 100 \mu\text{V}$ .
- If necessary, adjust with potentiometer R174.

## 4. Digital zero

- Depress button DC V (SK2/I).
- Short-circuit terminals BU1 and BU2.
- The display should show .000.
- If necessary, adjust with potentiometer R190.

## D. DC RANGES

- Depress button DC V (SK2/I).
- Adjust the d.c. ranges in accordance with the next calibration table.

Supply to BU1 - BU2	Adjust to	Adjust with	Polarity display
+1.900 V $\pm$ 0.01 %	1.900	R189	+
-1.900 V $\pm$ 0.01 %	1.900	R188	-
+19.00 V $\pm$ 0.01 %	19.00	R133	+
-19.00 V $\pm$ 0.01 %	19.00	-	-
+190.0 V $\pm$ 0.01 %	190.0	R134	+
-190.0 V $\pm$ 0.01 %	190.0	-	-
+1000 V $\pm$ 0.01 %	1000	R135	+
-1000 V $\pm$ 0.01 %	1000	-	-

## E. AC RANGES

- Depress button AC V (SK2/II).
- Adjust the a.c. ranges in accordance with the next calibration table

Supply to BU1 - BU2	Adjust with	Adjust to
1.900 V $\pm$ 0.05 %, 400 Hz	R172	1.900
350 V $\pm$ 0.05 %, 400 Hz	R173	350
1.900 V $\pm$ 0.05 %, 20 kHz	C309	1.900
19.00 V $\pm$ 0.05 %, 20 kHz	C310	19.00
190.0 V $\pm$ 0.05 %, 20 kHz	C311	190.0

## F. RESISTANCE RANGES

Adjust the resistance ranges in accordance with the next calibration table

Depress button	Connect to R input	Adjust with	Adjust to
M $\Omega$	1,8 M $\Omega \pm 0,1 \%$	R151	1,800
k $\Omega$	1,8 k $\Omega \pm 0,05 \%$	R152	1,800
k $\Omega$	180 k $\Omega \pm 0,05 \%$	R153	180,0
$\Omega$	180 $\Omega \pm 0,05 \%$	R154	180,0

Remarks

- When calibrating in the M $\Omega$ -ranges pay carefully attention to induction voltages.
- When calibrating in the  $\Omega$ -range the resistance of the measuring cable (typical 0,2  $\Omega$ ) should be added.

## G. CHECKING THE DC RANGES

- Depress button DC V (SK2/I).
- Check the DC ranges in accordance with the table below.

Supply to V input

terminals BU1 (+) and BU2 (-)	Display
Input short-circuited	0,000 + or - 1 digit
+ 10 mV	0,010 + or - 1 digit
+ 100 mV	0,100 + or - 1 digit
+ 1 V	1,000 + or - 2 digits
+ 1,9 V	1,900 + or - 3 digits
+ 19 V	19,00 + or - 3 digits
+ 190 V	190,0 + or - 3 digits
+1000 V	1000 + or - 2 digits

## H. CHECKING THE AC RANGES

- Depress button AC V (SK2/II).
- Check the DC ranges in accordance with the table below.

Supply to V input

terminals BU1 and BU2	Display
Input short-circuited	0,000 + 5 digits
1 V 400 Hz	1,000 + 6 digits - 8 digits
1,9 V 400 Hz	1,900 + 11 digits - 11 digits
1,9 V 20 kHz	1,900 + 11 digits - 11 digits
19 V 400 Hz	19,00 + 11 digits - 11 digits
19 V 20 kHz	19,00 + 11 digits - 11 digits
190 V 400 Hz	190,0 + 11 digits - 11 digits
190 V 20 kHz	190,0 + 11 digits - 11 digits
350 V 400 Hz	350 + 3 digits - 3 digits



## J. CHECKING THE RESISTANCE RANGES

Check the resistance ranges in accordance with the table below

Button depressed	Connect to input	Display
$\Omega$	input	0.000 $\pm$ 1 digit
k $\Omega$	short	0.000 $\pm$ 1 digit
M $\Omega$	circuited	0.000 $\pm$ 1 digit
$\Omega$	19 $\Omega$	019.0 $\pm$ 2 digits <sup>x</sup>
	190 $\Omega$	190.0 $\pm$ 11 digits <sup>x</sup>
k $\Omega$	1.9 k $\Omega$	1.900 $\pm$ 11 digits
	19 k $\Omega$	19.00 $\pm$ 11 digits
	190 k $\Omega$	190.0 $\pm$ 11 digits
M $\Omega$	1.9 M $\Omega$	1.900 $\pm$ 20 digits
	19 M $\Omega$	19.00 $\pm$ 39 digits

<sup>x</sup> Observe the resistance value of the measuring cable (about 0.2  $\Omega$ ).

## K. CHECKING THE AUTOMATIC RANGE SELECTION

- Depress button DC V.
- Supply to terminals BU1 and BU2 a variable d.c. voltage (0 up to 1000 V).
- Check the range selection in accordance with Fig. 31.

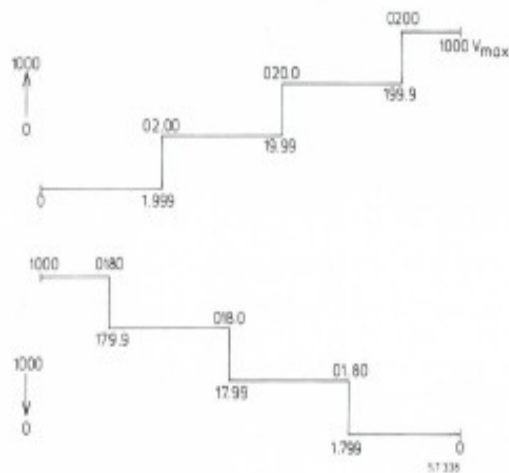


Fig. 31. Checking the automatic range selector

## L. CHECKING RANGE HOLD

When button RANGE HOLD is depressed the automatic range selector remains in the range set just before depressing button RANGE HOLD, no matter the magnitude of the input signal.

## M. CHECKING HOLD

When terminal HOLD at the rear is short-circuited the indication is hold also if the input value is changed.

## XI. List of parts

### A. MECHANICAL

Item	Fig.	Qty	Ordering number	Description
1	32	1	5322 456 14009	Filter plate
2	32	2	5322 528 34089	Fixed part
3	32	2	5322 528 34091	Movable part
4	32	2	5322 492 64345	Wave spring
5	32	2	5322 462 44132	Cover
6	32	2	5322 325 84326	Shaft
7	32	1	5322 414 24847	Knob
8	32	5	5322 414 24848	Knob
9	32	1	5322	Terminal BU3 (blue)
10	32	1	5322	Terminal BU2 (black)
11	32	1	5322	Terminal BU1 (red)
12	32	1	5322 456 14008	Dressing panel
13	32	1	5322 277 14055	Switch SK1
14	33	1	5322 290 40065	Earthing terminal BU4
15	33	1	5322 267 34021	Connector BU5
16	33	1	5322 256 40017	Fuse holder VL1
17	33	1	5322 325 54029	Cable grommet
18	33	1	5322 321 10071	Mains cable
19	33	2	5322 462 54073	Stand
20	34	3	5322 267 64018	Socket BU8...BU10
21	34+35	2	5322 268 24018	2x22-pole connector BU6, BU7
22	34	4	5322	Switch SK2/III... -/VI
23	34	2	5322 276 14082	Switch SK2/I and -/II
24	-	4	5322 462 54072	Foot

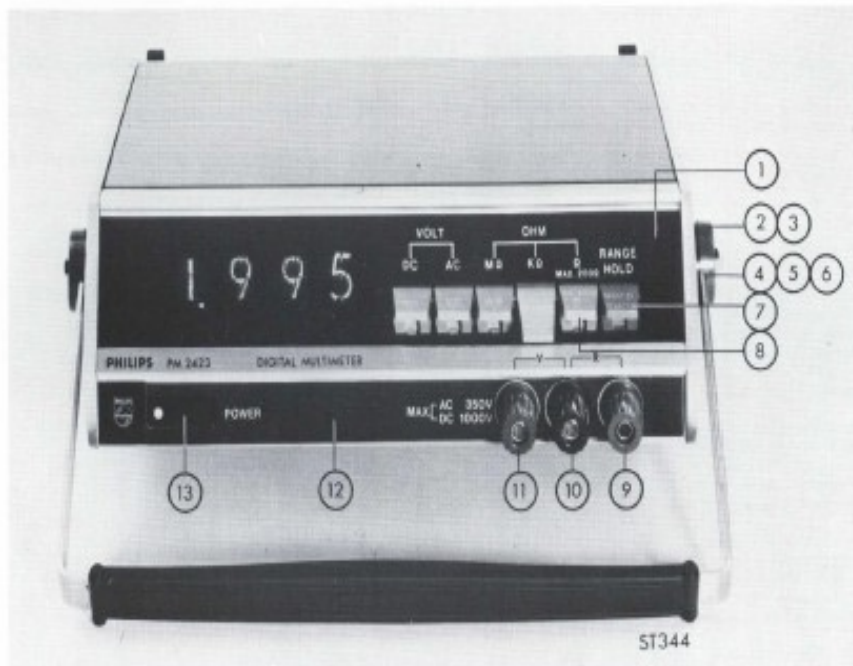


Fig. 32. Front view with item numbers

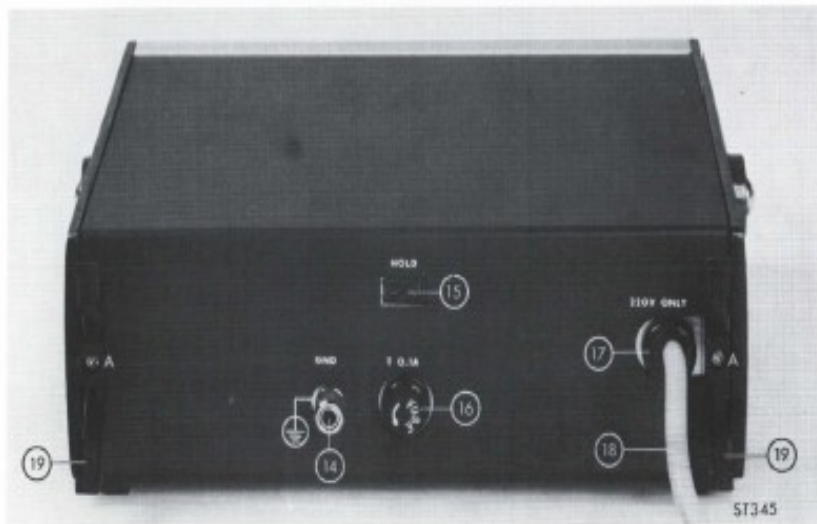


Fig. 33. Rear view with item numbers

**B. ELECTRICAL — ELEKTRISCH — ELEKTRISCH — ELECTRIQUE — ELECTRICOS**




















This parts list does not contain multi-purpose and standard parts. These components are indicated in the circuit diagram by means of identification marks. The specification can be derived from the survey below.

Diese Ersatzteilliste enthält keine Universal- und Standard-Teile. Diese sind im jeweiligen Prinzipschaltbild mit Kennzeichnungen versehen. Die Spezifikation kann aus nachstehender Übersicht abgeleitet werden.

In deze stuklijst zijn geen universele en standaardonderdelen opgenomen. Deze componenten zijn in het prinsipschema met een merkteken aangegeven. De specificatie van deze merktekens is hieronder vermeld.

La présente liste ne contient pas des pièces universelles et standard. Celles-ci ont été repérées dans le schéma de principe. Leurs spécifications sont indiquées ci-dessous.

Esta lista de componentes no comprende componentes universales ni standard. Estos componentes están provistos en el esquema de principio de una marca. El significado de estas marcas se indica a continuación.

	Carbon resistor E24 series Kohleschichtwiderstand, Reihe E24 Koolweerstand E24 reeks Résistance au carbone, série E24 Resistencia de carbón, serie E24	} 0,125 W	} 5%		Carbon resistor E12 series Kohleschichtwiderstand, Reihe E12 Koolweerstand E12 reeks Résistance au carbone, série E12 Resistencia de carbón, serie E12	} 1 W ≤ 2,2 MΩ, 5% > 2,2 MΩ, 10%
	Carbon resistor E12 series Kohleschichtwiderstand, Reihe E12 Koolweerstand E12 reeks Résistance au carbone, série E12 Resistencia de carbón, serie E12			} 0,25 W ≤ 1 MΩ, 5% > 1 MΩ, 10%		
	Carbon resistor E24 series Kohleschichtwiderstand, Reihe E24 Koolweerstand E24 reeks Résistance au carbone, série E24 Resistencia de carbón, serie E24	} 0,5 W ≤ 5 MΩ, 1% > 5 MΩ, 2% > 10 MΩ, 5%			Wire-wound resistor Drahtwiderstand Draadgewonden weerstand Résistance bobinée Resistencia bobinada	} 0,4 - 1,8 W
	Carbon resistor E12 series Kohleschichtwiderstand, Reihe E12 Koolweerstand E12 reeks Résistance au carbone, série E12 Resistencia de carbón, serie E12		} 0,5 W ≤ 1,5 MΩ, 5% > 1,5 MΩ, 10%		Wire-wound resistor Drahtwiderstand Draadgewonden weerstand Résistance bobinée Resistencia bobinada	
	Wire-wound resistor Drahtwiderstand Draadgewonden weerstand Résistance bobinée Resistencia bobinada	} 10 W		} 5%		
	Tubular ceramic capacitor Rohrkondensator Keramische kondensator, buistype Condensateur céramique tubulaire Condensador cerámico tubular		} 500 V			Polyester capacitor Polyesterkondensator Polyesterkondensator Condensateur au polyester Condensador polyester
	Tubular ceramic capacitor Rohrkondensator Keramische kondensator, buistype Condensateur céramique tubulaire Condensador cerámico tubular	} 700 V			Flat-foil polyester capacitor Miniatur-Polyesterkondensator (flach) Plaque miniatur polyesterkondensator Condensateur au polyester, type plat Condensador polyester, tipo de placas planas	} 250 V
	Ceramic capacitor, "pin-up" Keramikkondensator "Pin-up" (Perltyp) Keramische kondensator "Pin-up" type Condensateur céramique, type perl Condensador cerámico, versión "colgable"		} 500 V		Paper capacitor Papierkondensator Papierkondensator Condensateur au papier Condensador de papel	
	"Microplate" ceramic capacitor Miniatur-Scheibenkondensator "Microplate" keramische kondensator Condensateur céramique "microplaque" Condensador cerámico "microplaca"	} 30 V			Wire-wound trimmer Drahttrimmer Draadgewonden trimmer Trimmer à fil Trimmer bobinado	
	Mica capacitor Glimmerkondensator Micakondensator Condensateur au mica Condensador de mica		} 500 V		Tubular ceramic trimmer Rohrtrimmer Buisvormige keramische trimmer Trimmer céramique tubulaire Trimmer cerámico tubular	



For multi-purpose and standard parts, please see PHILIPS' Service Catalogue.

Für die Universal- und Standard-Teile siehe den PHILIPS Service-Katalog.

Voor universele en standaardonderdelen raadplege men de PHILIPS Service Catalogue.

Pour les pièces universelles et standard veuillez consulter le Catalogue Service PHILIPS.

Para piezas universales y standard consulte el Catálogo de Servicio PHILIPS.

Resistors

No.	Ordering number	Value	%	Watt	Description
R121	5322 116 54298	5 MΩ	0.5	1	Metal film
R122	5322 116 54299	3.9 MΩ	0.5	1	Metal film
R123	5322 116 54301	1 MΩ	0.1	1	Metal film
R124	5322 116 54301	1 MΩ	0.5	1	Metal film
R125	5322 116 54302	108.6 kΩ	0.5	0.25	Metal film
R126	5322 116 54303	9.85 kΩ	0.5	0.25	Metal film
R127	5322 116 50113	150 kΩ	5	0.25	Metal film
R128	5322 116 50113	150 kΩ	5	0.25	Metal film
R129	5322 116 54155	100 kΩ	5	0.25	Metal film
R130	5322 116 54155	100 kΩ	5	0.25	Metal film
R133	5322 101 14035	200 kΩ			Potentiometer
R134	5322 101 14036	5 kΩ			Potentiometer
R135	5322 101 14037	500 Ω			Potentiometer
R136	5322 101 14038	10 kΩ			Potentiometer
R139	5322 116 54304	7.9 MΩ	1	1	Metal film
R140	5322 116 54305	7.9 kΩ	1	0.25	Metal film
R143	5322 116 54306	72 kΩ	1	0.25	Metal film
R144	5322 116 54307	790 Ω	1	0.5	Metal film
R145	5322 116 50113	150 kΩ	5	0.25	Metal film
R146	5322 116 50113	150 kΩ	5	0.25	Metal film
R147	5322 116 54155	100 kΩ	5	0.25	Metal film
R148	5322 116 54155	100 kΩ	5	0.25	Metal film
R151	5322 101 14039	1 MΩ			Potentiometer
R152	5322 101 14041	1 kΩ			Potentiometer
R153	5322 101 14038	10 kΩ			Potentiometer
R154	5322 100 10138	100 Ω			Potentiometer
R166	5322 116 50748	10 kΩ	1	0.1	Metal film
R167	5322 116 50748	10 kΩ	1	0.1	Metal film
R168	5322 116 54308	40 kΩ	1	0.1	Metal film
R169	5322 116 54309	84 kΩ	1	0.1	Metal film
R170	5322 116 54311	80 kΩ	1	0.1	Metal film
R171	5322 101 14038	10 kΩ			Potentiometer
R172	5322 101 14038	10 kΩ			Potentiometer
R173	5322 101 14036	5 kΩ			Potentiometer
R174	5322 101 14038	10 kΩ			Potentiometer
R175	5322 116 54196	34 kΩ	1	0.1	Metal film
R176	5322 116 54312	4.2 kΩ	1	0.1	Metal film
R180	5322 116 50198	110 kΩ	1	0.125	Metal film
R181	5322 116 50198	110 kΩ		0.125	Metal film
R182	5322 116 50729	4.2 kΩ	5	0.25	Metal film
R183	5322 116 54313	62 kΩ	1	0.1	Metal film
R184	5322 116 50748	10 kΩ	1	0.1	Metal film
R185	5322 116 50729	4.2 kΩ	5	0.25	Metal film
R186	5322 116 54313	62 kΩ	1	0.1	Metal film
R187	5322 116 50748	10 kΩ	1	0.1	Metal film

No.	Ordering number	Value	%	Watt	Description
R188	5322 101 14036	5 k $\Omega$			Potentiometer
R189	5322 101 14036	5 k $\Omega$			Potentiometer
R190	5322 101 14038	10 k $\Omega$			Potentiometer
R227	5322 116 50117	4.7 k $\Omega$	1	0.25	Metal film
R228	5322 116 50117	4.7 k $\Omega$	1	0.25	Metal film
R229	5322 116 50649	68 k $\Omega$	1	0.25	Metal film
R230	5322 116 50649	68 k $\Omega$	1	0.25	Metal film
R232	5322 101 14042	20 k $\Omega$			Potentiometer
R234	5322 209 84221	10x100 k $\Omega$			Composition
R235	5322 209 84221	10x100 k $\Omega$			Composition
R236	5322 209 84221	10x100 k $\Omega$			Composition
R248	5322 209 84222	5x3.3 k $\Omega$			Composition
R249	5322 209 84222	5x3.3 k $\Omega$			Composition
R276	5322 116 50117	4.7 k $\Omega$	1	0.25	Metal film
R277	5322 116 54314	5.9 k $\Omega$	1	0.25	Metal film
R283	5322 116 50936	18 k $\Omega$	1	0.25	Metal film
R284	5322 116 54202	7.5 k $\Omega$	1	0.25	Metal film
R287	5322 116 50117	4.7 k $\Omega$	1	0.25	Metal film
R288	5322 116 50657	6.8 k $\Omega$	1	0.25	Metal film
R290	5322 116 54202	7.5 k $\Omega$	1	0.25	Metal film
R291	5322 101 14041	1 k $\Omega$			Potentiometer
R292	5322 116 50657	6.8 k $\Omega$	1	0.25	Metal film
R294	5322 209 84222	5x3.3 k $\Omega$			Composition

#### Capacitors

No.	Ordering number	Value	%	V	Description
C1	5322 122 34019	10 nF		500	Ceramic
C2	5322 122 34019	10 nF		500	Ceramic
C301	4822 121 40226	220 nF		600	Polyester
C302	5322 121 44106	2.2 nF		100	Mylar
C303	5322 123 34008	2 pF	10	500	Mica
C304	5322 123 34008	2 pF	10	500	Mica
C305	5322 123 34009	7 pF	10	300	Mica
C306	5322 123 34011	15 pF	10	300	Mica
C307	5322 123 34012	130 pF	10	300	Mica
C308	5322 123 34013	68 pF	10	300	Mica
C309	5322 125 54004	6 pF			Trimmer
C310	5322 125 54005	20 pF			Trimmer
C311	5322 125 54005	20 pF			Trimmer
C312	4822 122 40007	22 pF	10		Ceramic
C313	5322 122 34021	47 nF			Ceramic
C314	5322 122 30103	22 nF			Ceramic
C315	4822 122 30055	330 pF	10		Ceramic
C316	4822 124 20575	100 $\mu$ F		16	Electrolytic

No.	Ordering number	Value	%	Watt	Description
C318	5322 122 30103	22 nF			Ceramic
C319	5322 122 30002	150 pF	10		Ceramic
C320	5322 122 34021	47 nF			Ceramic
C321	5322 121 40013	1 $\mu$ F	10	200	Polyester
C322	5322 122 30003	3 pF	10		Ceramic
C323	5322 122 30016	33 pF	10		Ceramic
C324	5322 122 34021	47 nF			Ceramic
C325	5322 122 30103	22 nF			Ceramic
C326	5322 121 40061	220 nF	10	200	Polyester
C327	5322 122 30016	33 pF	10		Ceramic
C328	5322 122 30027	1 nF			Ceramic
C329	5322 122 34021	47 nF			Ceramic
C330	5322 122 30027	1 nF			Ceramic
C331	5322 122 34021	47 nF			Ceramic
C332	5322 122 34021	47 nF			Ceramic
C333	5322 122 30021	100 pF	10		Ceramic
C334	5322 122 30021	100 pF	10		Ceramic
C335	5322 122 30021	100 pF	10		Ceramic
C336	5322 122 30021	100 pF	10		Ceramic
C337	5322 122 30034	470 pF	10		Ceramic
C338	5322 122 30034	470 pF	10		Ceramic
C339	5322 122 34021	47 nF			Ceramic
C340	5322 122 34021	47 nF			Ceramic
C341	5322 121 54051	1000 pF	5	50	Metal film
C342	5322 121 54051	1000 pF	5	50	Metal film
C343	5322 122 30101	220 pF	10		Ceramic
C344	5322 122 30129	4.7 nF			Ceramic
C345	5322 122 34021	47 nF			Ceramic
C346	5322 123 34014	20 pF		300	Mica
C348	5322 122 34021	47 nF			Ceramic
C349	5322 122 34021	47 nF			Ceramic
C350	5322 122 30027	1 nF			Ceramic
C351	5322 122 30129	4.7 nF			Ceramic
C352	5322 122 34021	47 nF			Ceramic
C353	4822 124 20575	100 $\mu$ F		25	Electrolytic
C354	4822 124 20355	10 $\mu$ F		25	Electrolytic
C355	4822 124 20355	10 $\mu$ F		25	Electrolytic
C356	4822 124 20355	10 $\mu$ F		16	Electrolytic
C357	5322 122 34021	47 nF			Ceramic
C358	4822 124 20575	100 $\mu$ F		25	Electrolytic
C359	5322 122 34022	100 nF			Ceramic
C360	4822 124 20368	33 $\mu$ F		10	Electrolytic
C361	5322 122 34021	47 nF			Ceramic
C362	4822 124 20417	1000 $\mu$ F		16	Electrolytic
C363	4822 124 20065	2.2 $\mu$ F		315	Electrolytic
C366	4822 124 20355	10 $\mu$ F		16	Electrolytic

Miscellaneous

Item	Ordering number	Description
T1	5322 146 34022	Power transformer
U1	5322 216 64091	Printed wiring board
U2	5322 216 64092	Printed wiring board
U3	5322 216 64093	Printed wiring board
VLI	5322 253 30006	Mains fuse 100 mA, delayed action
B371	5322 131 94016	Indicator tube
B372	5322 131 94016	Indicator tube
B373	5322 131 94016	Indicator tube
LA381	5322 134 24011	Neon lamp
LA382	5322 134 24011	Neon lamp
LA383	5322 134 24011	Neon lamp
LA384	5322 134 24011	Neon lamp
RE391	5322 280 24034	Reed relay
RE392	5322 280 24035	Reed relay
RE393	5322 280 24035	Reed relay
RE394	5322 280 24035	Reed relay
RE395	5322 280 24035	Reed relay
RE396	5322 280 24035	Reed relay
RE397	5322 280 24035	Reed relay
L1	5322 158 14045	Filter coil

## SEMICONDUCTORS

Integrated circuits

No.	Type	Ordering number	Replacement type	Ordering number	Remark
IC1	LM301A	5322 209 84214	-	-	Selected (see VII.C.1)
IC2	LM301A	5322 209 84214	-	-	Selected (see VII.C.1)
IC3	LM301A	5322 209 84215	-	-	Selected (see VII.C.2)
IC4	LM301A	5322 209 84216	-	-	Selected (see VII.C.3)
IC5	LM311	5322 209 84217	-	-	
IC6	SN74141N (Texas Instr)		N7441 B	5322 209 84159	
IC7	SN74141N (Texas Instr)		N7441 B	5322 209 84159	
IC8	SN74141N (Texas Instr)		N7441 B	5322 209 84159	
IC9	SN7475N (Texas Instr)		FJJ181	5322 209 84047	
IC10	SN7475N (Texas Instr)		FJJ181	5322 209 84047	
IC11	SN7475N (Texas Instr)		FJJ181	5322 209 84047	



No.	Type	Ordering number	Replacement type	Ordering number	Remark
IC12	SN7402N (Texas Instr)		FJH221	5322 209 84145	
IC13	SN7475N (Texas Instr)		FJJ181	5322 209 84047	
IC14	SN7400N (Texas Instr)		FJH131	5322 209 84143	
IC15	SN7490N (Texas Instr)		FJJ141	5322 209 84114	
IC16	SN7490N (Texas Instr)		FJJ141	5322 209 84114	
IC17	SN7490N (Texas Instr)		FJJ141	5322 209 84114	
IC18	SN7405N (Texas Instr)		FJH251	5322 130 30613	
IC19	SN7401N (Texas Instr)		FJH231	5322 209 84218	
IC20	SN7474N (Texas Instr)		FJJ131	5322 209 80065	
IC21	SN7401N (Texas Instr)		FJH231	5322 209 84218	
IC22	SN7404N (Texas Instr)		FJH241	5322 209 84147	
IC23	SN7410N (Texas Instr)		FJH121	5322 209 84153	
IC24	SN7473N (Texas Instr)		FJJ121	5322 209 84142	
IC25	SN7400N (Texas Instr)		FJH131	5322 209 84143	
IC26	SN7473N (Texas Instr)		FJJ121	5322 209 84142	
IC27	SN7412N (Texas Instr)	5322 209 84219			
IC28	SN7400N (Texas Instr)		FJH131	5322 209 84143	
IC29	SN7401N (Texas Instr)		FJH231	5322 209 84218	
IC30	SN7401N (Texas Instr)		FJH231	5322 209 84218	

Transistors

No.	Ordering number	Type	Replacement type	Ordering number	
TS31	5322 130 44166	ITS3948	-	-	Dual FET
TS32	5322 130 44166	ITS3948	-	-	Dual FET
TS33	5322 130 44167	NF510	-	-	FET
TS34	5322 130 44167	NF510	-	-	FET
TS35	5322 130 44167	NF510	-	-	FET
TS36	5322 130 44167	NF510	-	-	FET
TS37	5322 130 44167	NF510	-	-	FET
TS38	-	2SA495	2N2907A	5322 130 40621	
TS39	-	2SA495	2N2907A	5322 130 40621	
TS40	-	2SA495	2N2907A	5322 130 40621	
TS41	-	2SA495	2N2907A	5322 130 40621	
TS42	5322 130 44168	2SC269	-	-	
TS43	5322 130 44168	2SC269	-	-	
TS44	5322 130 44168	2SC269	-	-	
TS45	5322 130 44168	2SC269	-	-	
TS46	5322 130 44168	2SC269	-	-	
TS47	5322 130 44169	2SC268A	-	-	
TS48	5322 130 44169	2SC268A	-	-	
TS49	5322 130 44169	2SC268A	-	-	
TS50	5322 130 44169	2SC268A	-	-	
TS51	5322 130 44169	2SC268A	-	-	
TS52	5322 130 44169	2SC268A	-	-	
TS53	5322 130 44169	2SC268A	-	-	
TS54	5322 130 44169	2SC268A	-	-	
TS55	5322 130 44168	2SC269	-	-	
TS56	5322 130 44168	2SC269	-	-	
TS57	-	2SA495	2N2907A	5322 130 40621	
TS58	-	2SA495	2N2907A	5322 130 40621	
TS59	5322 130 44168	2SC269			
TS60	5322 130 44168	2SC269			
TS61	5322 130 44168	2SC269			
TS62	5322 130 44171	2SC1014			
TS63	5322 130 44168	2SC269			
TS64	5322 130 44168	2SC269			
TS65	5322 130 44168	2SC269			
TS66	5322 130 44171	2SC1014			
TS67	5322 130 44168	2SC269			
TS68	5322 130 44168	2SC269			
TS1		2SD315D	BD124	5322 130 40504	

No.	Ordering number	Type	Replacement type	Ordering number	
<u>Dioden</u>					
GR70		WG713	BAW62	5322 130 30613	
GR71	5322 130 34175	IS1516			Selected (see VII. C. 4)
GR72	5322 130 34175	IS1516			Selected (see VII. C. 4)
GR73	5322 130 34176	SD-101			
GR74	5322 130 34176	SD-101			
GR75	5322 130 34176	SD-101			
GR76	5322 130 34176	SD-101			
GR77	5322 130 34177	F-14B			
GR78	5322 130 34177	F-14B			
GR79	5322 130 34177	F-14B			
GR80	5322 130 34177	F-14B			
GR81	5322 130 34178	1S2192			
GR82	5322 130 34178	1S2192			
GR83		WG713	BAW62	5322 130 30613	
GR84		WG713	BAW62	5322 130 30613	
GR85		WG713	BAW62	5322 130 30613	
GR86		WG713	BAW62	5322 130 30613	
GR87		WG713	BAW62	5322 130 30613	
GR88		WG713	BAW62	5322 130 30613	
GR89		WG713	BAW62	5322 130 30613	
GR90		WG713	BAW62	5322 130 30613	
GR91		WG713	BAW62	5322 130 30613	
GR92		WG713	BAW62	5322 130 30613	
GR93		WG713	BAW62	5322 130 30613	
GR94		WG713	BAW62	5322 130 30613	
GR95		WG713	BAW62	5322 130 30613	
GR96		WG713	BAW62	5322 130 30613	
GR97		WG713	BAW62	5322 130 30613	
GR98		WG713	BAW62	5322 130 30613	
GR99		WG713	BAW62	5322 130 30613	
GR100		WG713	BAW62	5322 130 30613	
GR101		WG713	BAW62	5322 130 30613	
GR102		WG713	BAW62	5322 130 30613	
GR103		WG713	BAW62	5322 130 30613	
GR104		WG713	BAW62	5322 130 30613	
GR105		WG713	BAW62	5322 130 30613	
GR106		WG713	BAW62	5322 130 30613	
GR107		WG713	BAW62	5322 130 30613	
GR108		WG713	BAW62	5322 130 30613	
GR109	5322 130 34179	1S1850R			Dual diode
GR110	5322 130 34181	1S1850			Dual diode
GR111	5322 130 34179	1S1850R			Dual diode
GR112	5322 130 34181	1S1850			Dual diode

No.	Ordering number	Type	Replacement type	Ordering number
GR113	5322 130 34179	1S1850R		Dual diode
GR114	5322 130 34181	1S1850		Dual diode
GR115	5322 130 34182	1S1851R		Dual diode
GR116	5322 130 34183	1S1851		Dual diode
GR117	5322 130 34184	WG713		
GR118	5322 130 34184	WG713		
GR119	5322 130 34185	RD-9A		

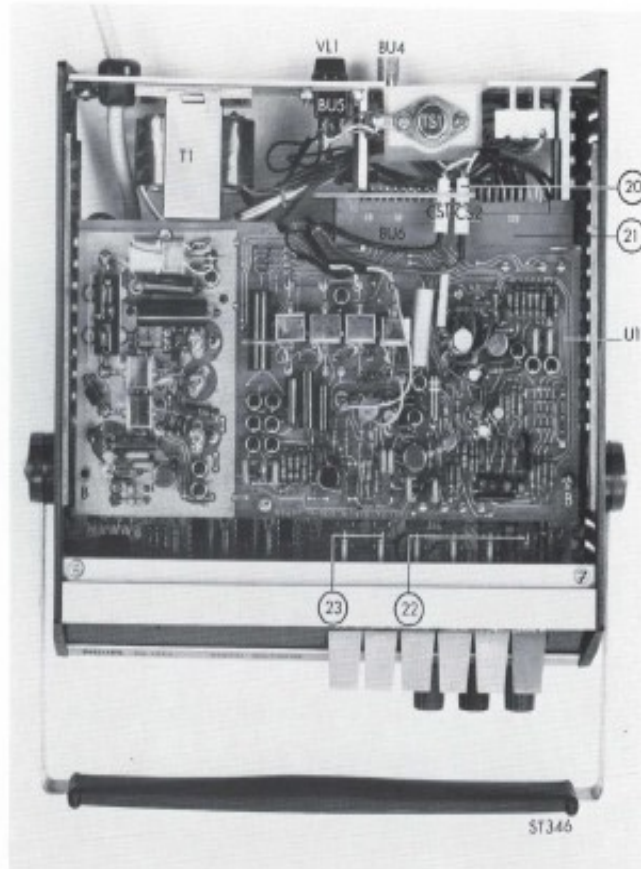


Fig. 34. Top view (cabinet removed)

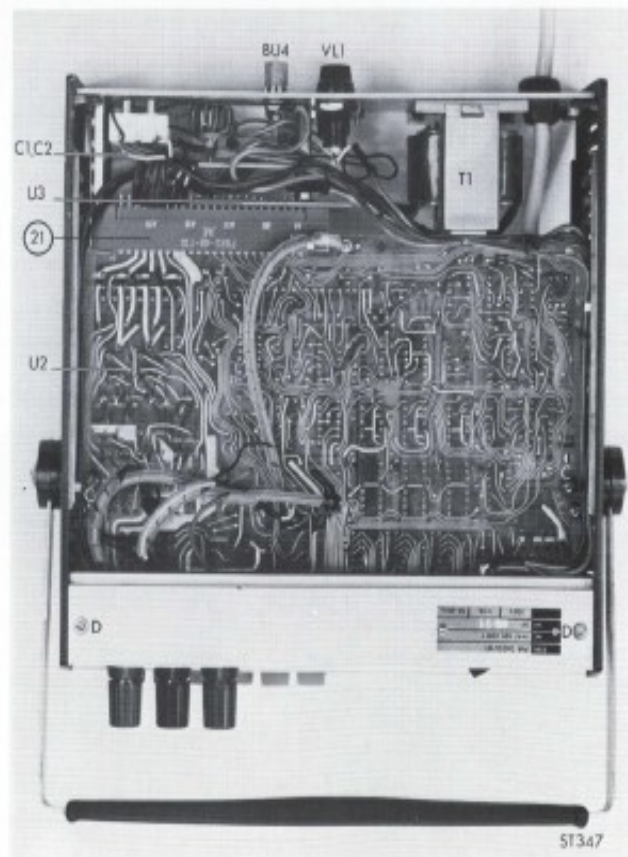


Fig. 35. Bottom view (cabinet removed)

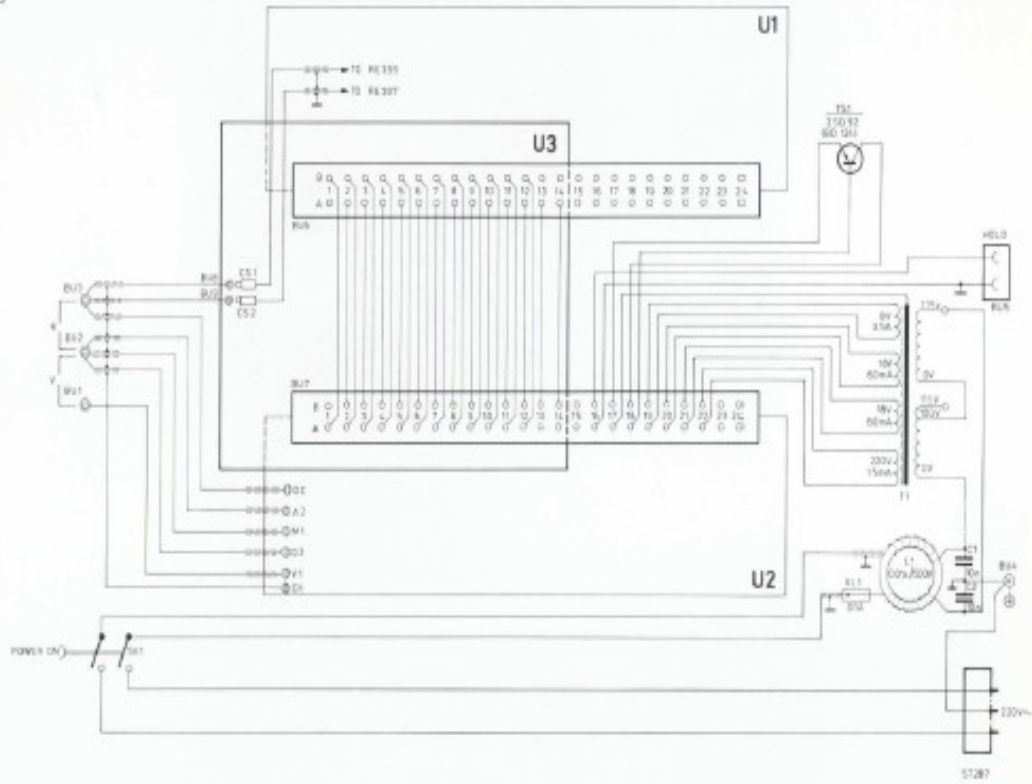


Fig. 36. Wiring diagram

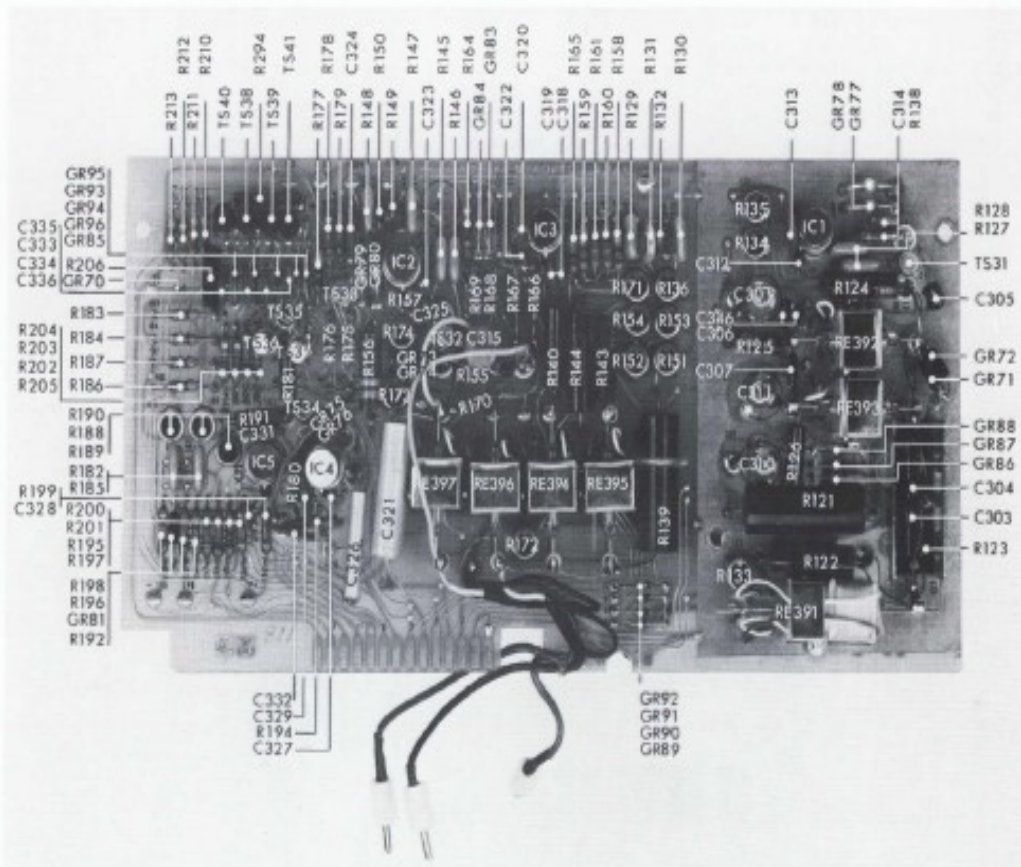


Fig. 37. Printed wiring board U1

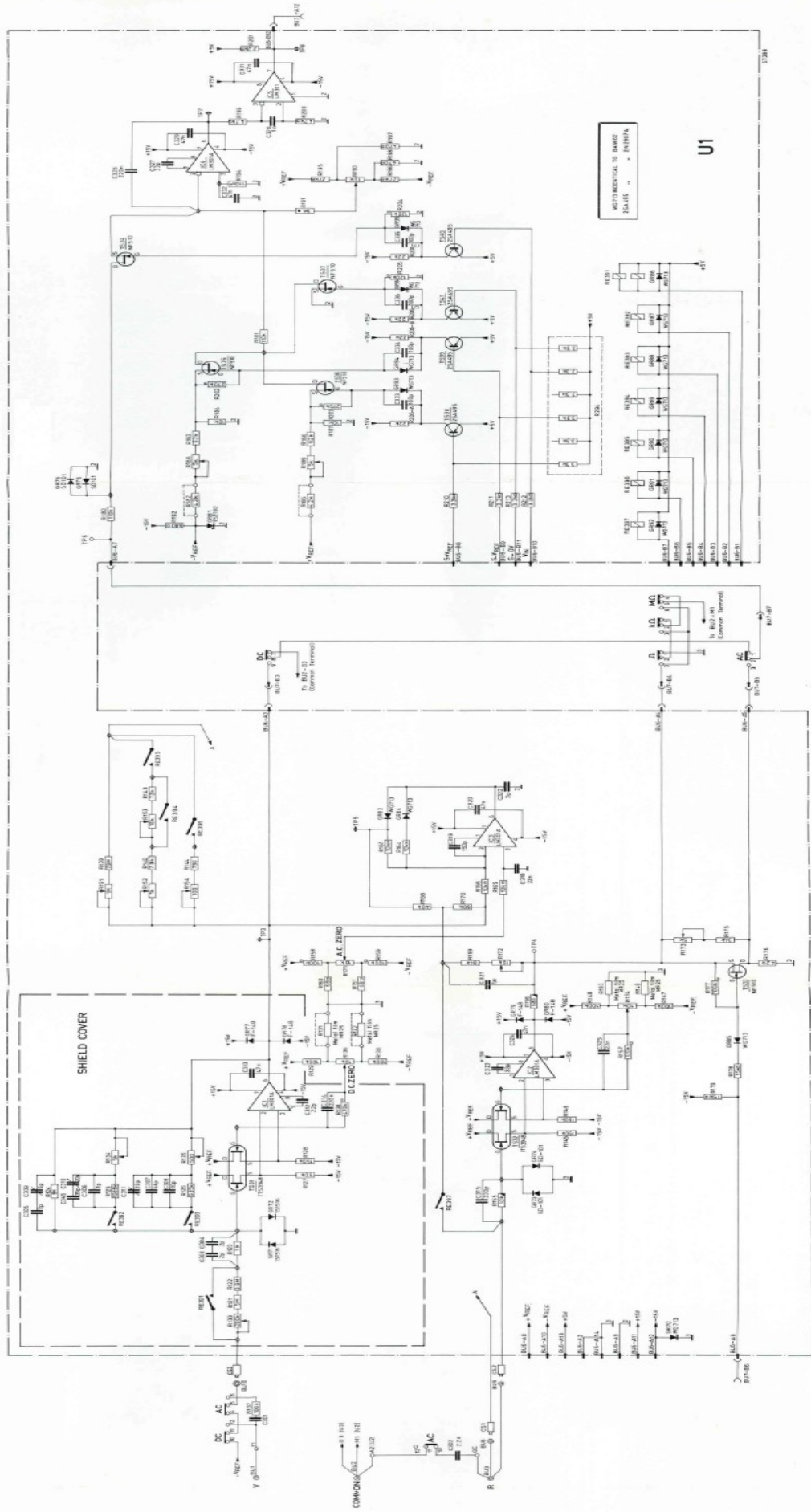


Fig. 38. Circuit diagram analog section U1

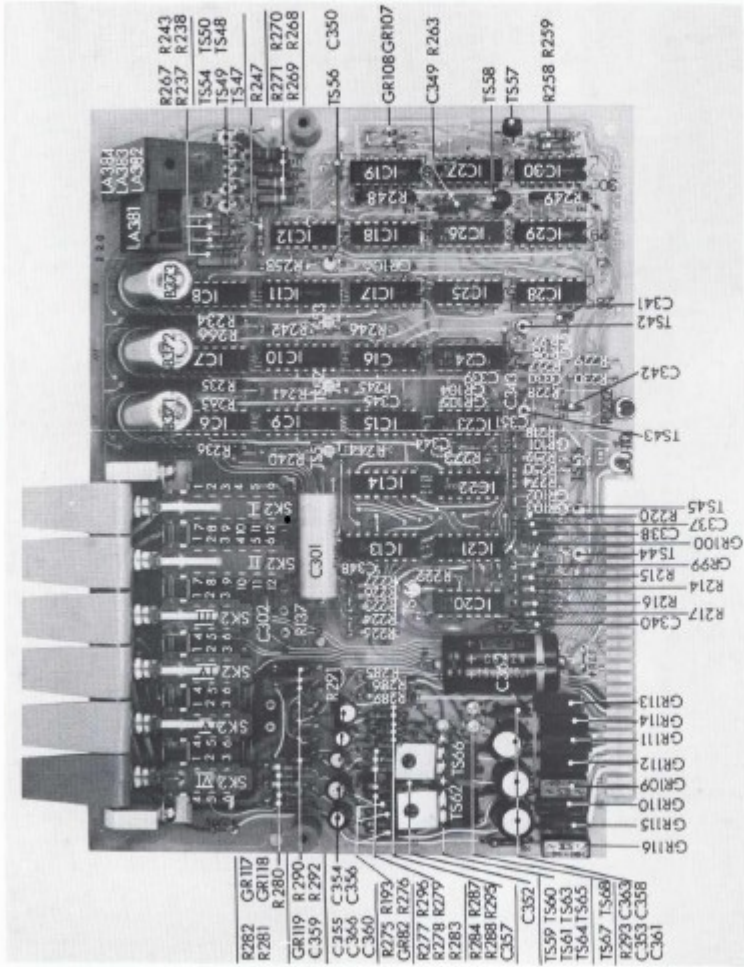


Fig. 39. Printed wiring board U2



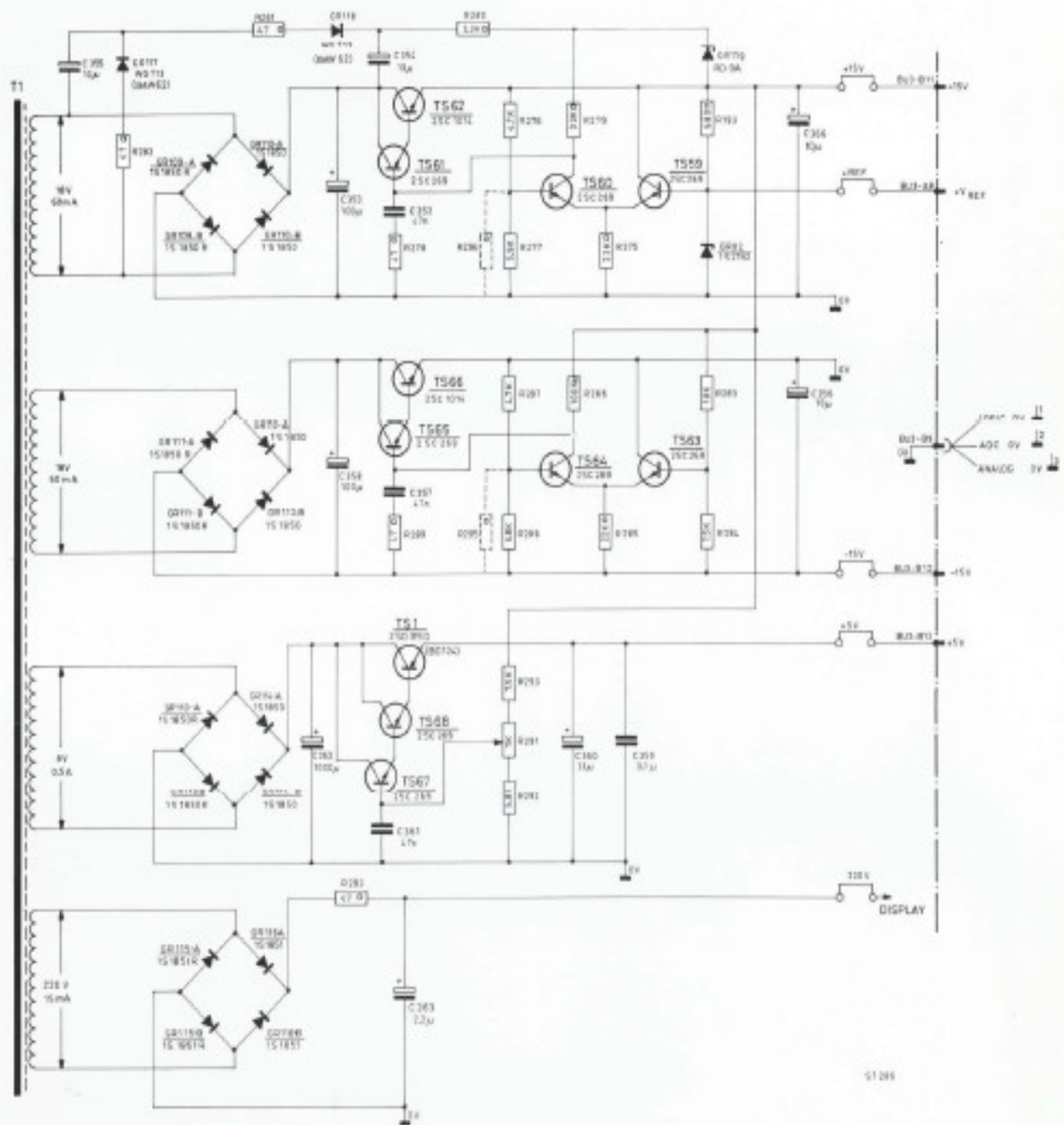


Fig. 40. Circuit diagram power supply

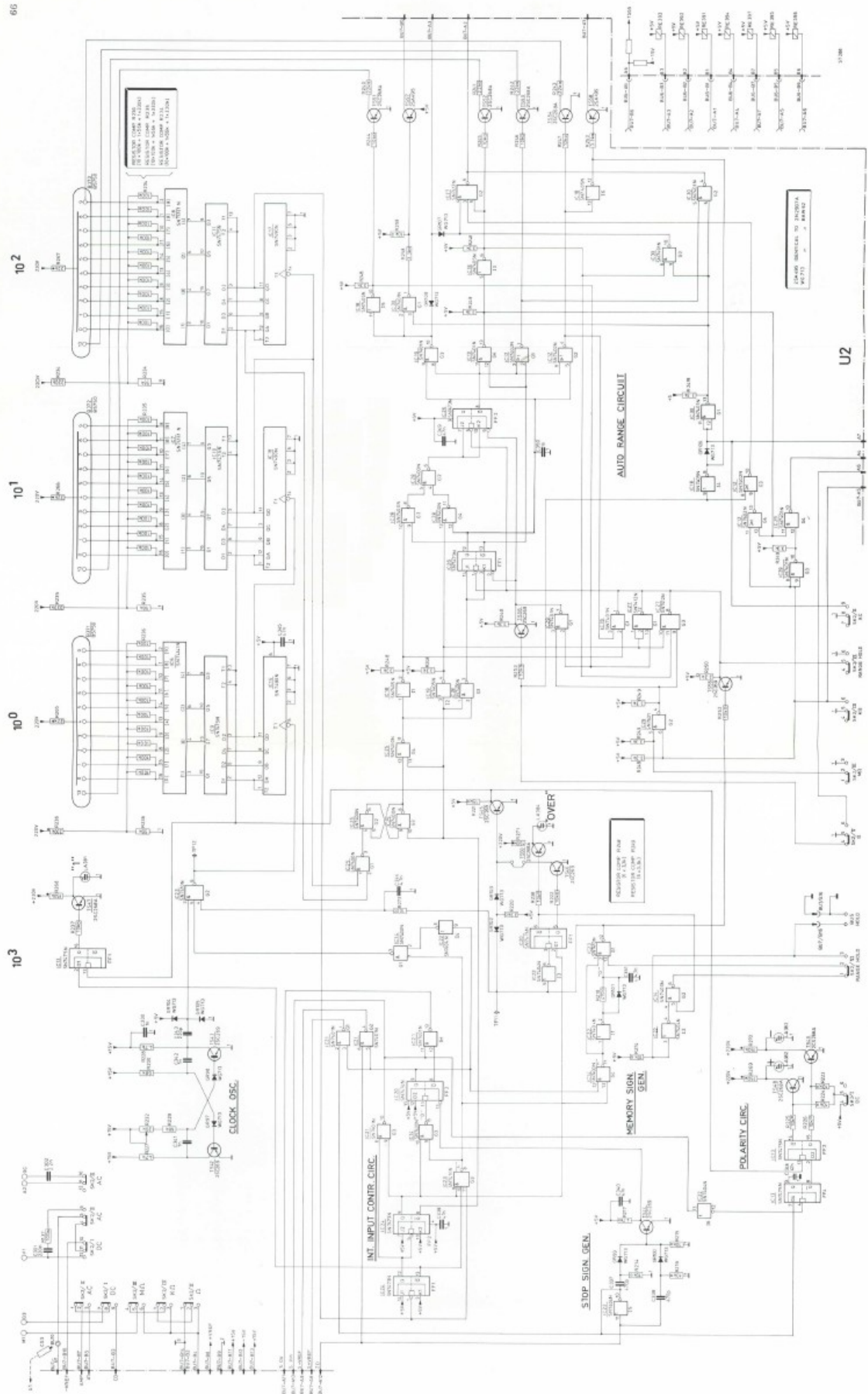


Fig. 41. Circuit diagram digital section

## QUALITY REPORTING

### CODING SYSTEM FOR FAILURE DESCRIPTION

The following information is meant for Philips service workshops only and serves as a guide for exact reporting of service repairs and maintenance routines on the workshop charts.

For full details reference is made to Information G1 (Introduction) and Information Cd 689 (Specific information for Test and Measuring Instruments).

#### LOCATION

□□□□

Unit number

e.g. 000A or 0001 (for unit A or 1; not 00UA or 00U1)

or: Type number of an accessory (only if delivered with the equipment)

e.g. 9051 or 9532 (for PM 9051 or PM 9532)

or: Unknown/Not applicable

0000

#### COMPONENT/SEQUENCE NUMBER

□□□□

Enter the identification as used in the circuit diagram, e.g.:

GR1003	Diode GR1003
TS0023	Transistor TS23
IC0101	Integrated circuit IC101
R0....	Resistor, potentiometer
C0....	Capacitor, variable capacitor
B0....	Tube, valve
LA....	Lamp
VL....	Fuse
SK....	Switch
BU....	Connector, socket, terminal
TO....	Transformer
LO....	Coil
X0....	Crystal
CB....	Circuit block
RE....	Relay
ME....	Meter, indicator
BA....	Battery
TR....	Chopper

#### CATEGORY

□

- 0 Unknown, not applicable (fault not present, intermittent or disappeared)
- 1 Software error
- 2 Readjustment
- 3 Electrical repair (wiring, solder joint, etc.)
- 4 Mechanical repair (polishing, filing, remachining, etc.)
- 5 Replacement
- 6 Cleaning and/or lubrication
- 7 Operator error
- 8 Missing items (on pre-sale test)
- 9 Environmental requirements are not met

Parts not identified in the circuit diagram:

990000	Unknown/Not applicable
990001	Cabinet or rack (text plate, emblem, grip, rail, graticule, etc.)
990002	Knob (incl. dial knob, cap, etc.)
990003	Probe (only if attached to instrument)
990004	Leads and associated plugs
990005	Holder (valve, transistor, fuse, board, etc.)
990006	Complete unit (p.w. board, h.t. unit, etc.)
990007	Accessory (only those without type number)
990008	Documentation (manual, supplement, etc.)
990009	Foreign object
990099	Miscellaneous

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**Zambia:** Philips Electrical Ltd., Professional Equipment Division, P.O.B. 553 Kitwe; tel. 2526/7/8; Lusaka P.O. Box 1878

730101

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